

FIG. 1

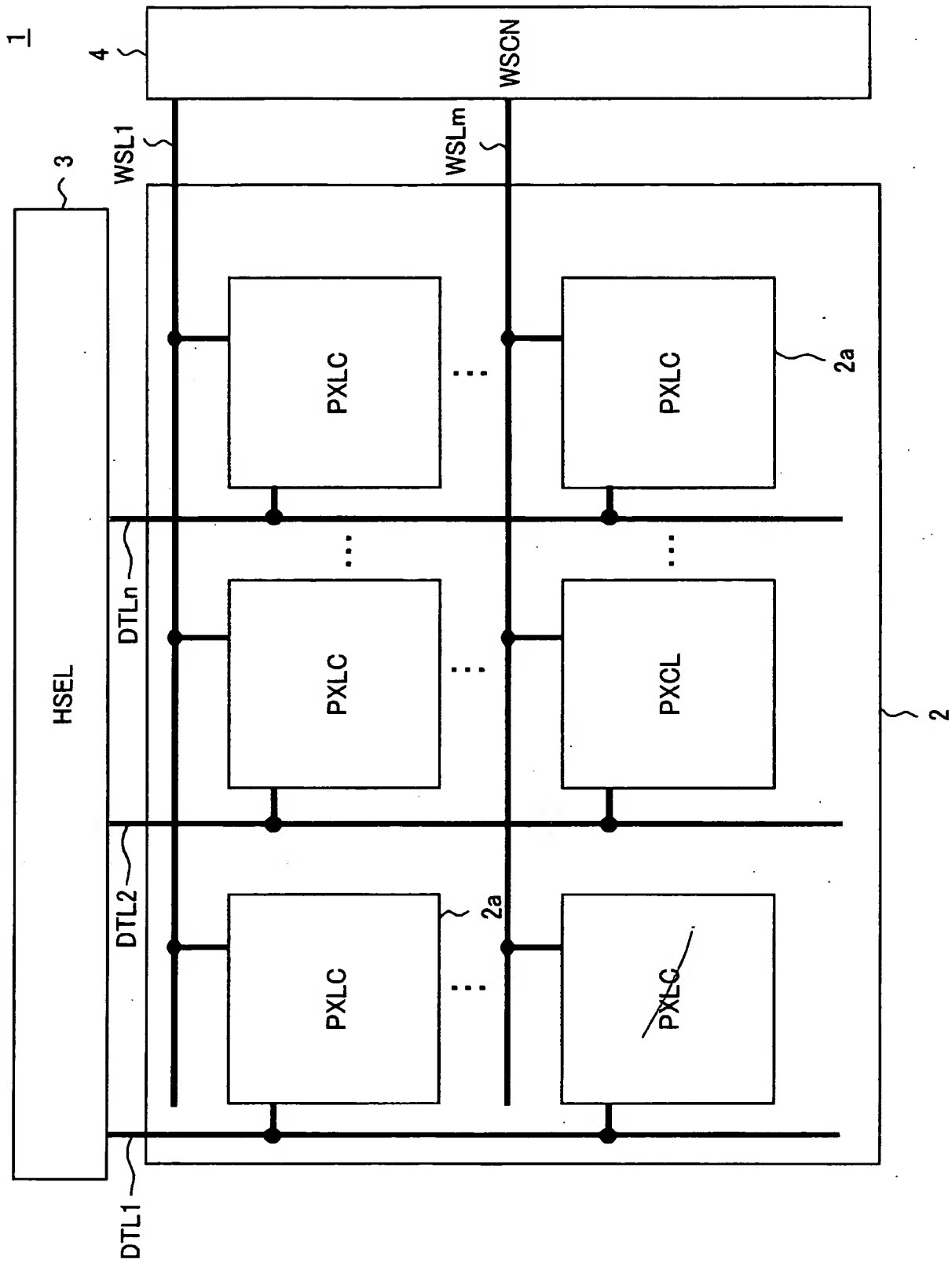


FIG. 2

2a

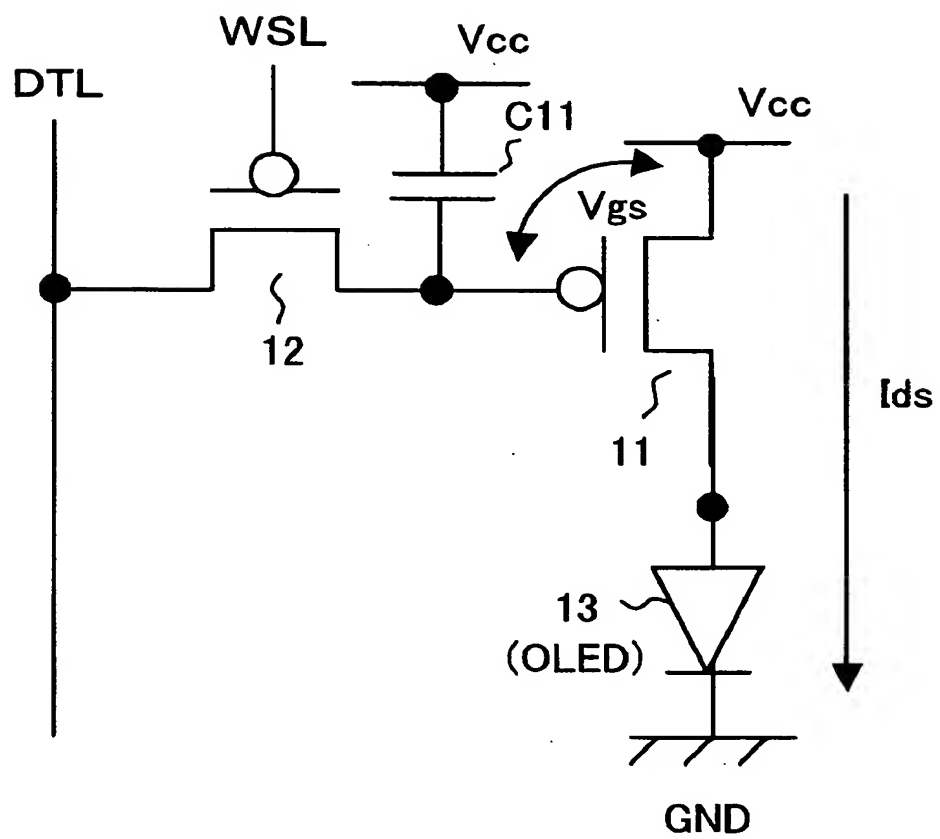


FIG. 3

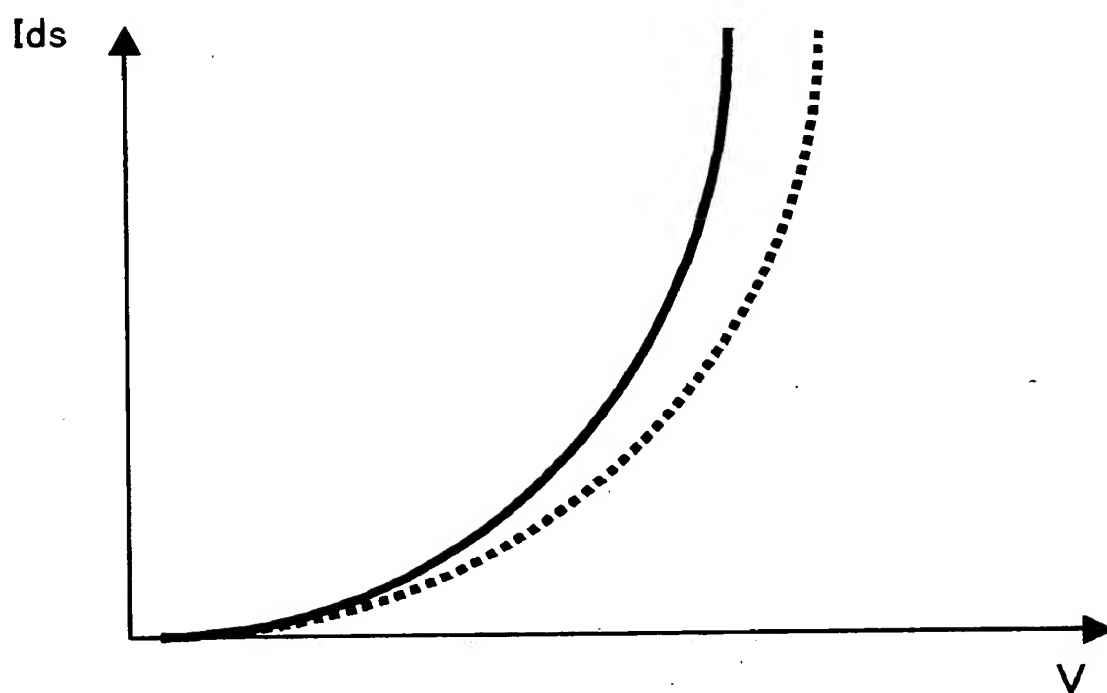


FIG. 4

2b

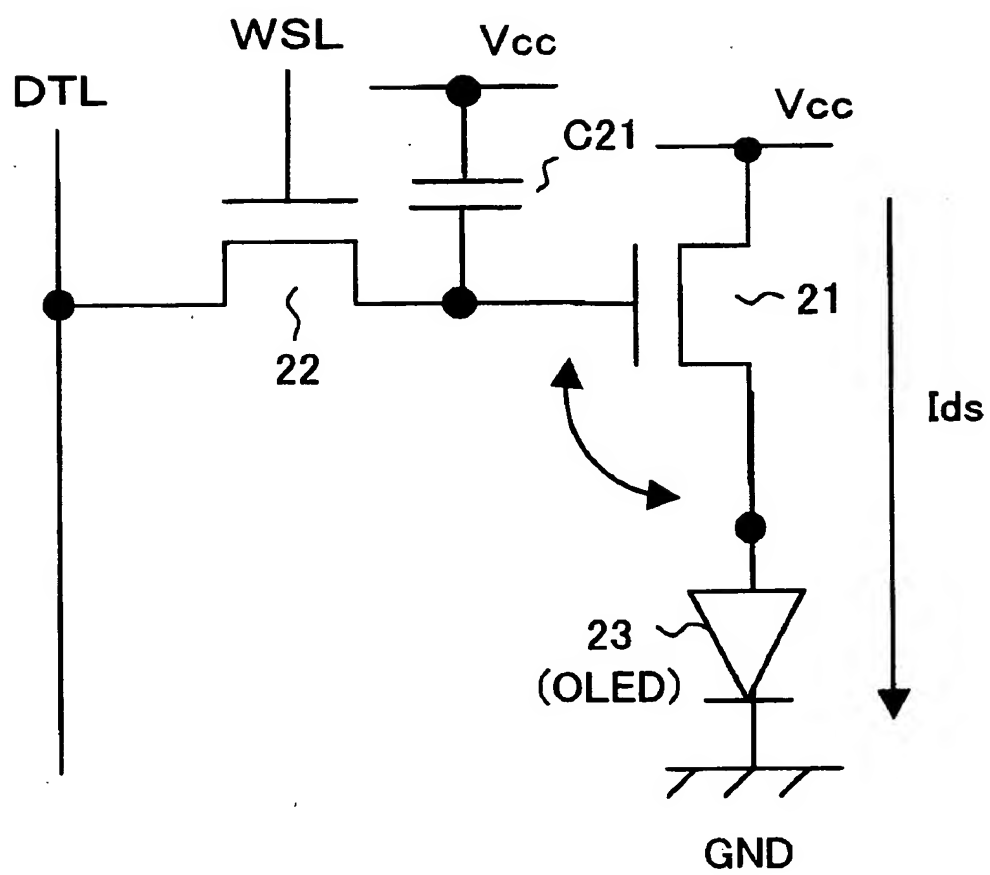


FIG. 5

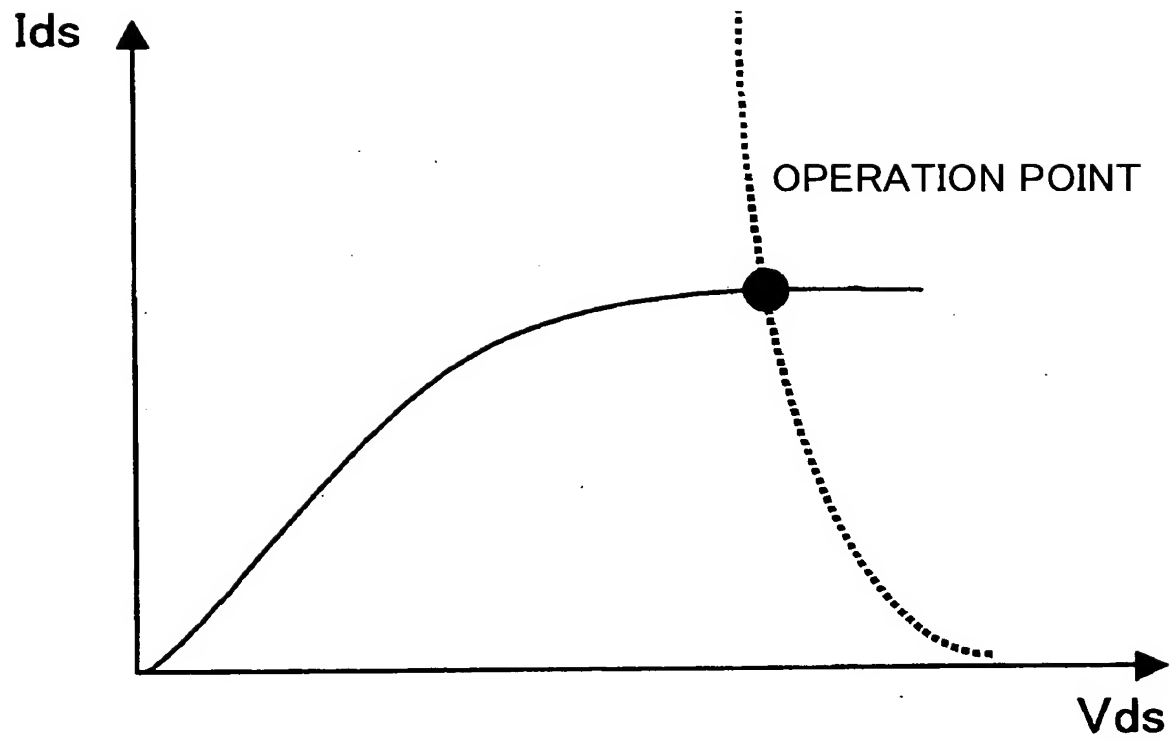


FIG. 6

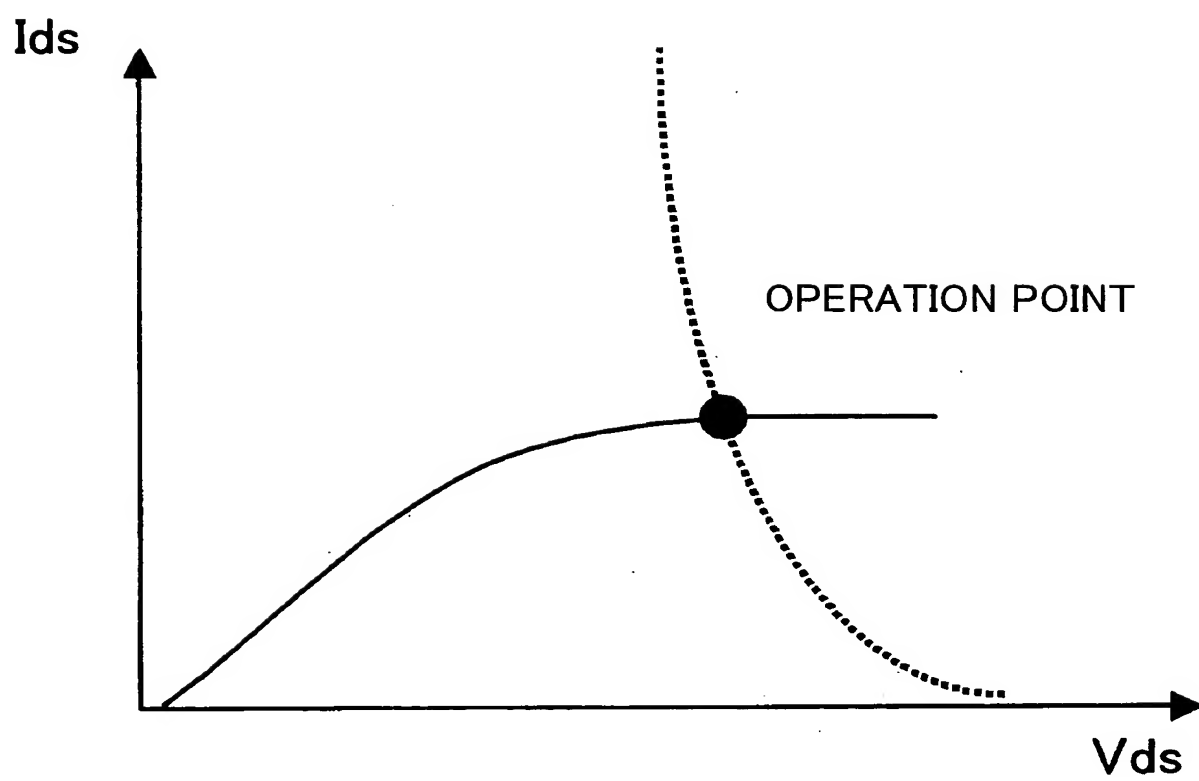


FIG. 7

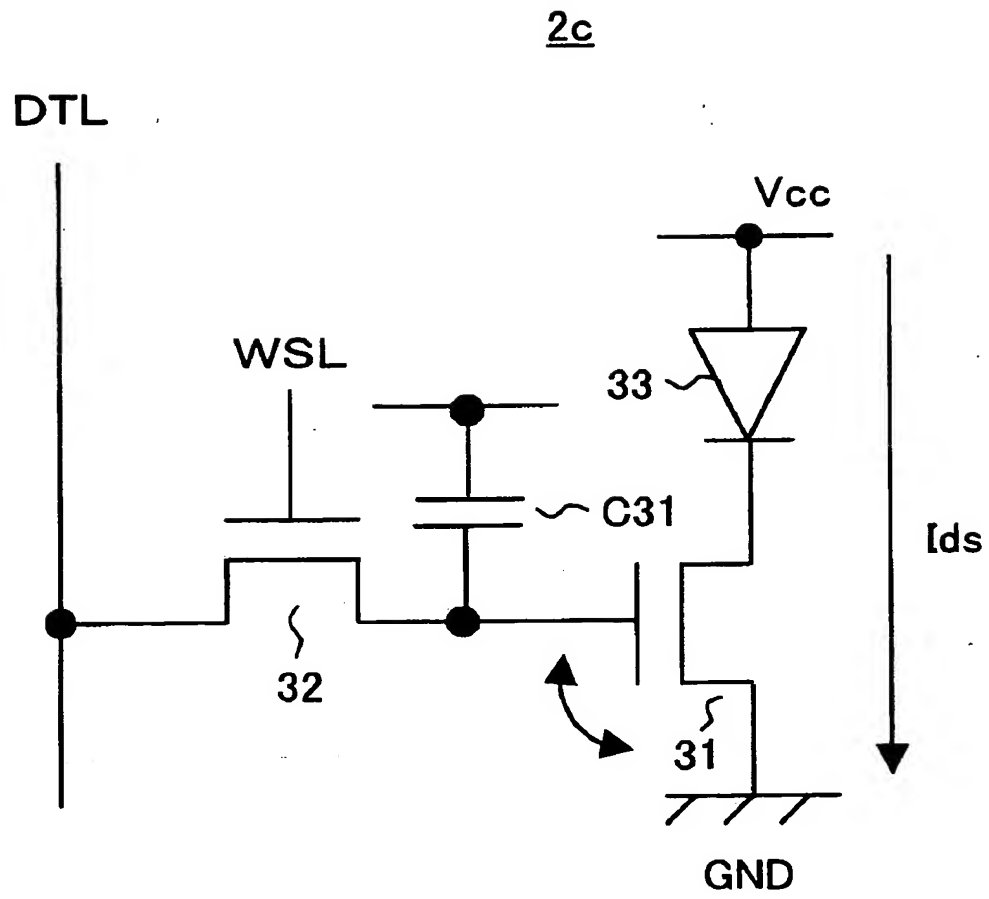


FIG. 8

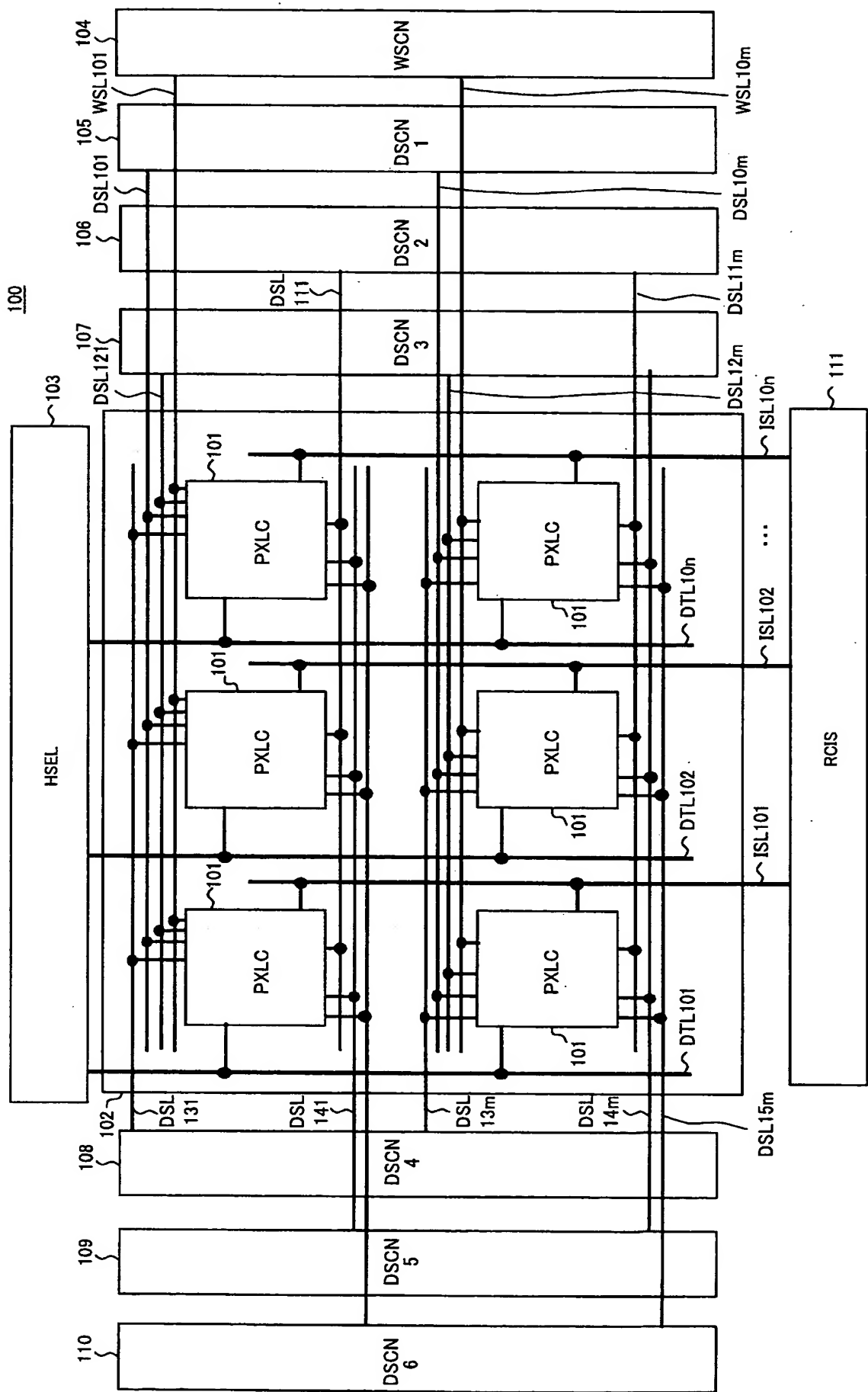


FIG. 9

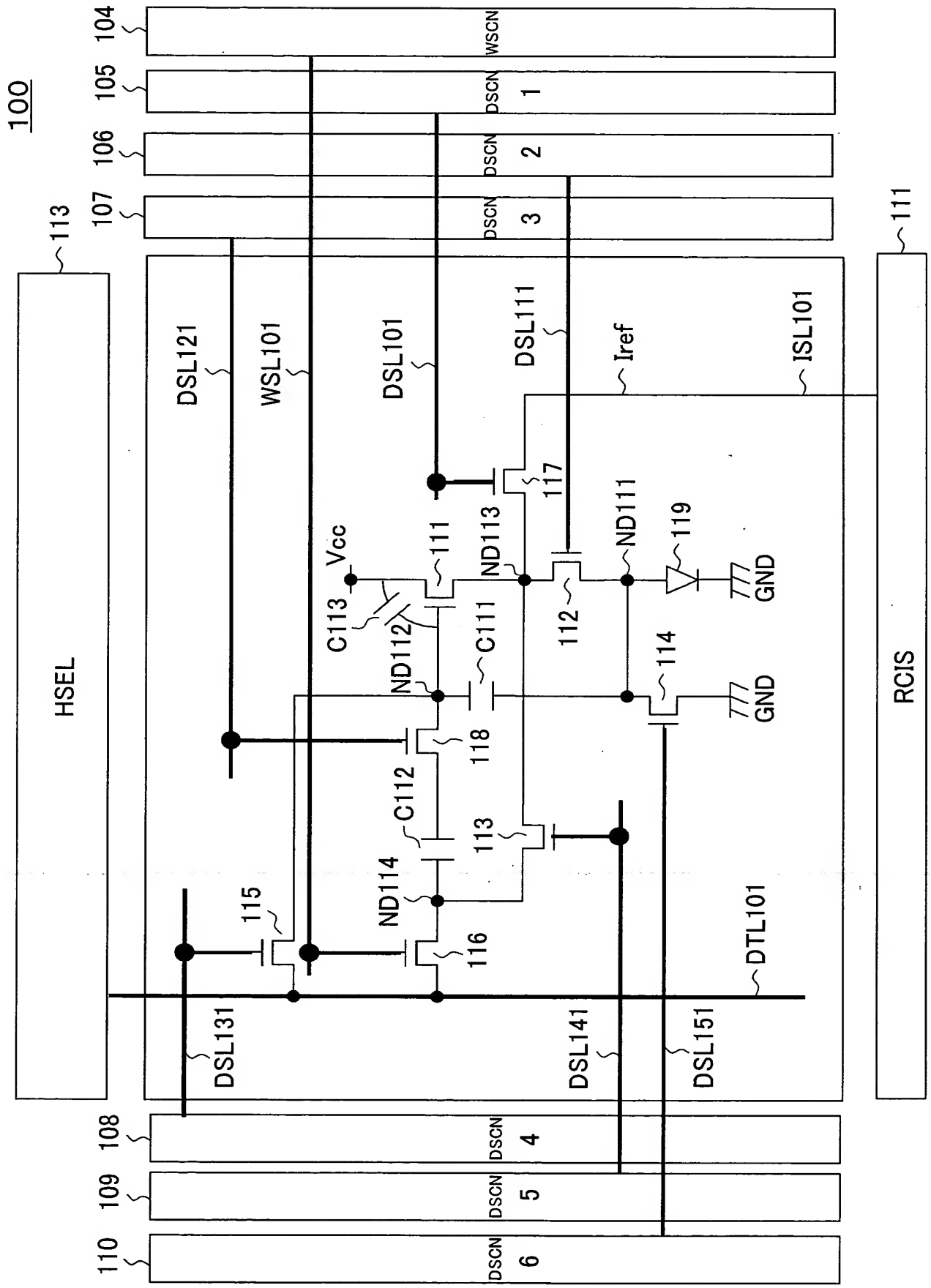


FIG. 10A $ds[4]$ TFT115

FIG. 10B $ws[1]$ TFT116

FIG. 10C $ds[3]$ TFT118

FIG. 10D $ds[5]$ TFT113

FIG. 10E $ds[6]$ TFT114

FIG. 10F $ds[2]$ TFT112

FIG. 10G $ds[1]$ TFT111

FIG. 10H V_{g111}

FIG. 10I V_{ND111}

THRESHOLD VALUE OF ORGANIC EL

GND LEVEL

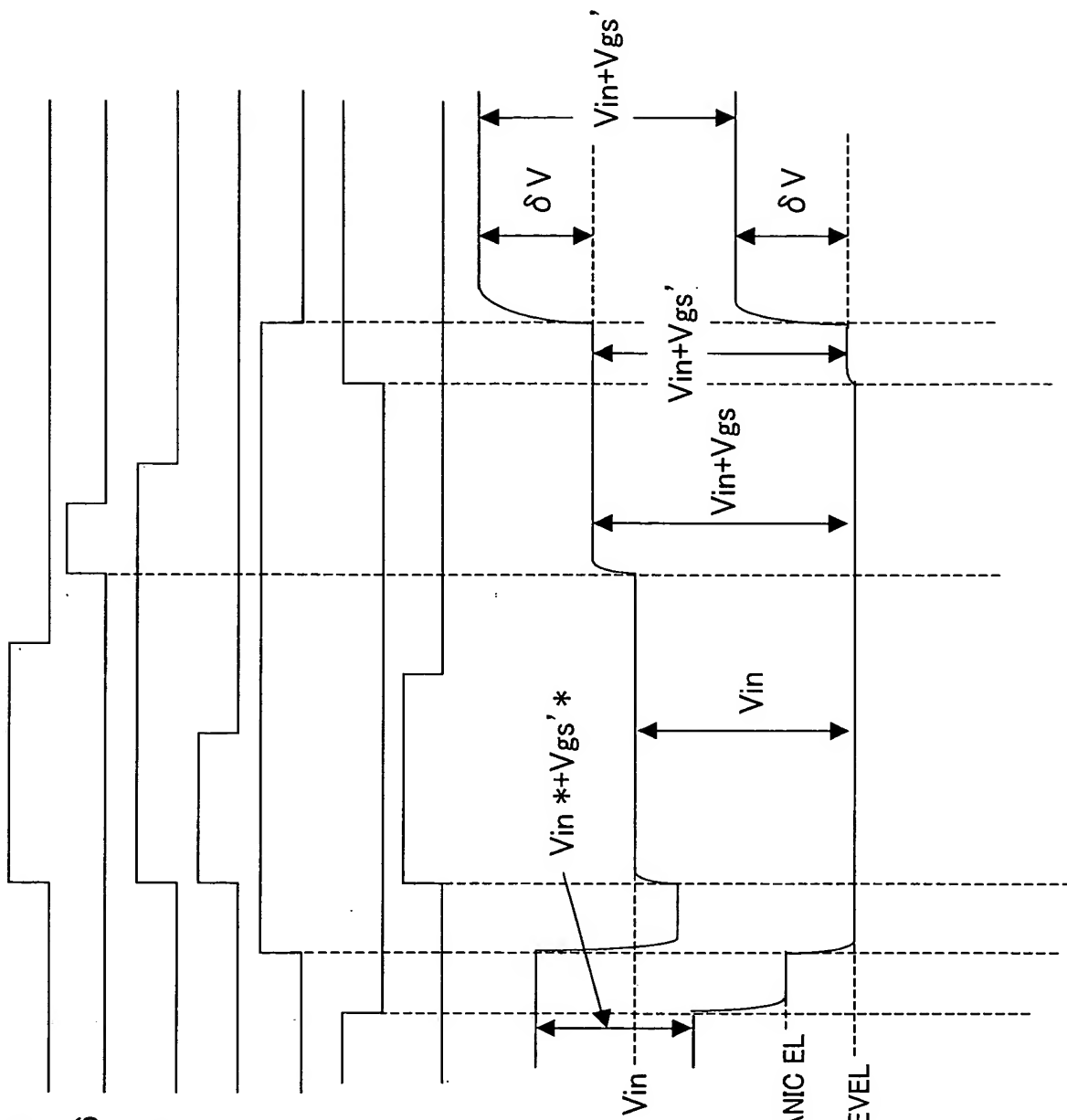
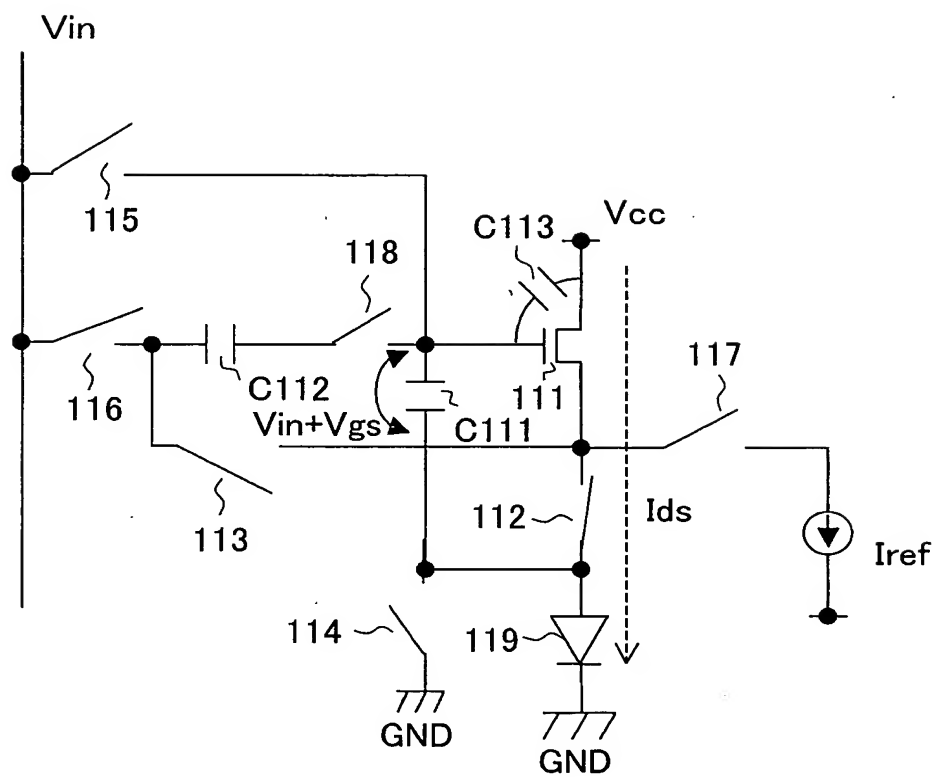


FIG. 13



INPUT SIGNAL (V_{in})

FIG. 14

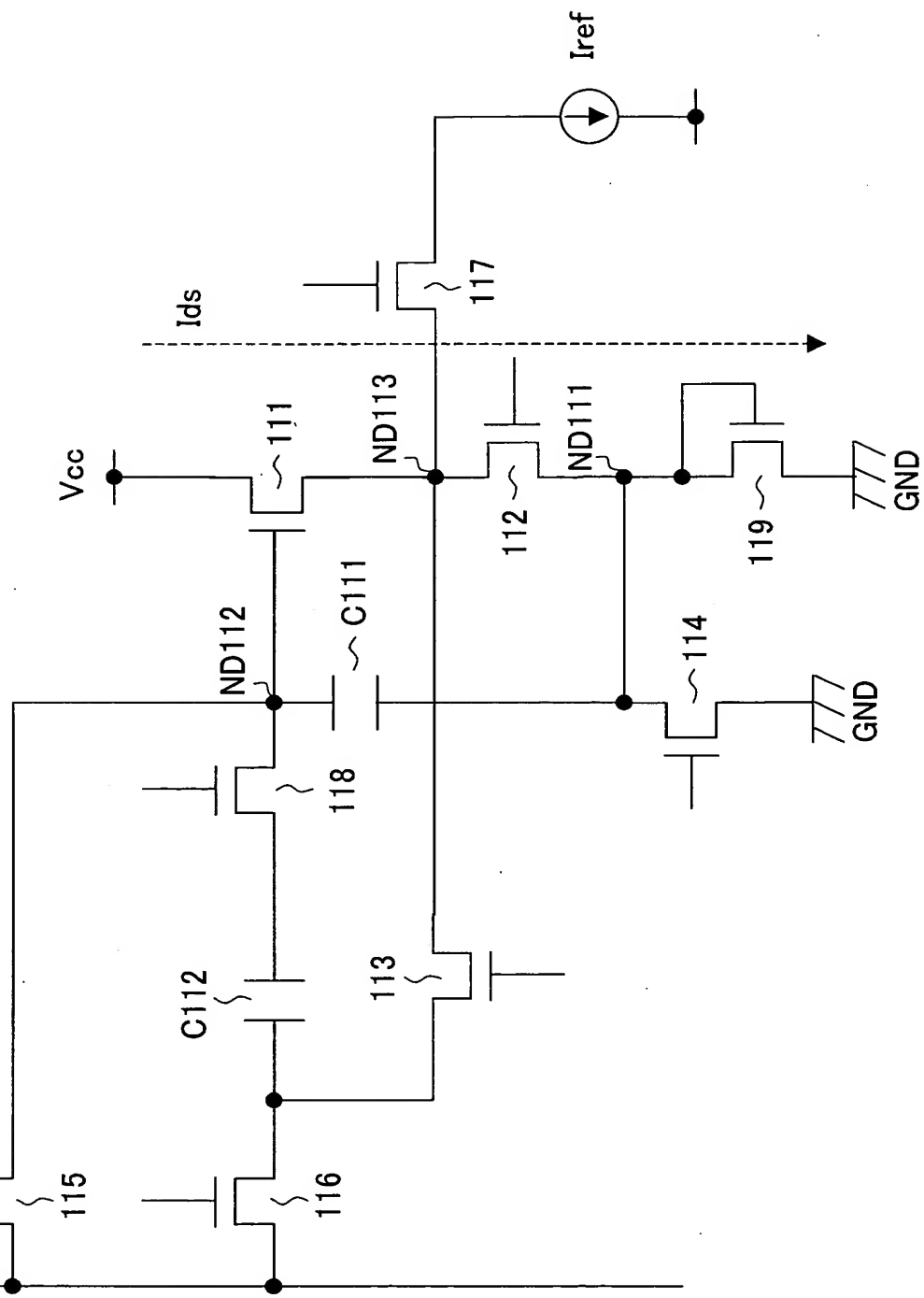


FIG. 15

GATE VOLTAGE OF
DRIVE TRANSISTOR

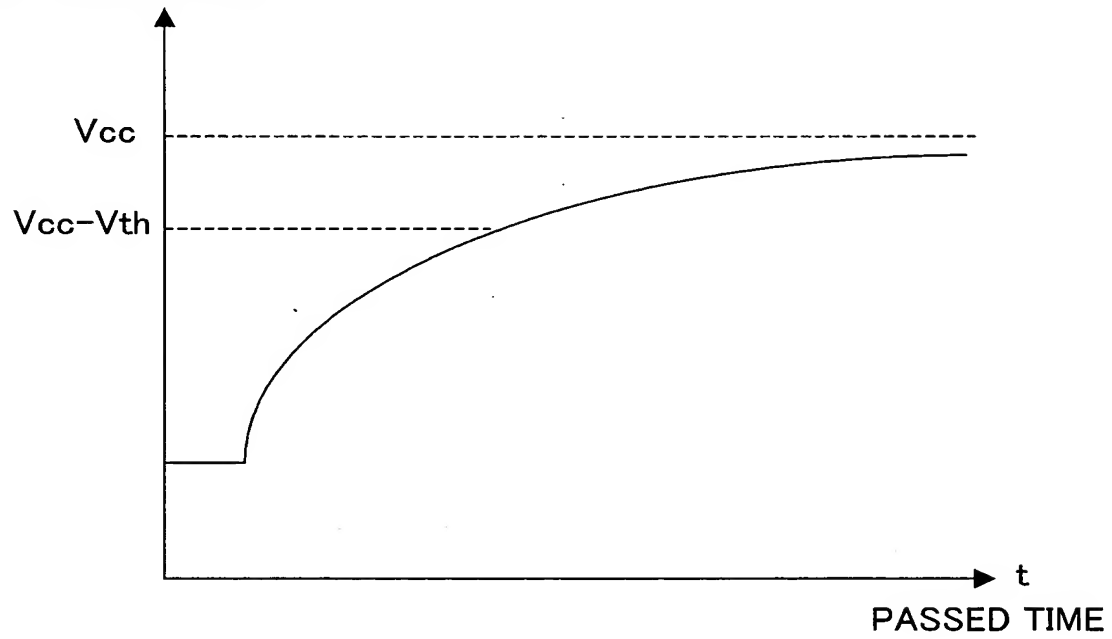


FIG. 16

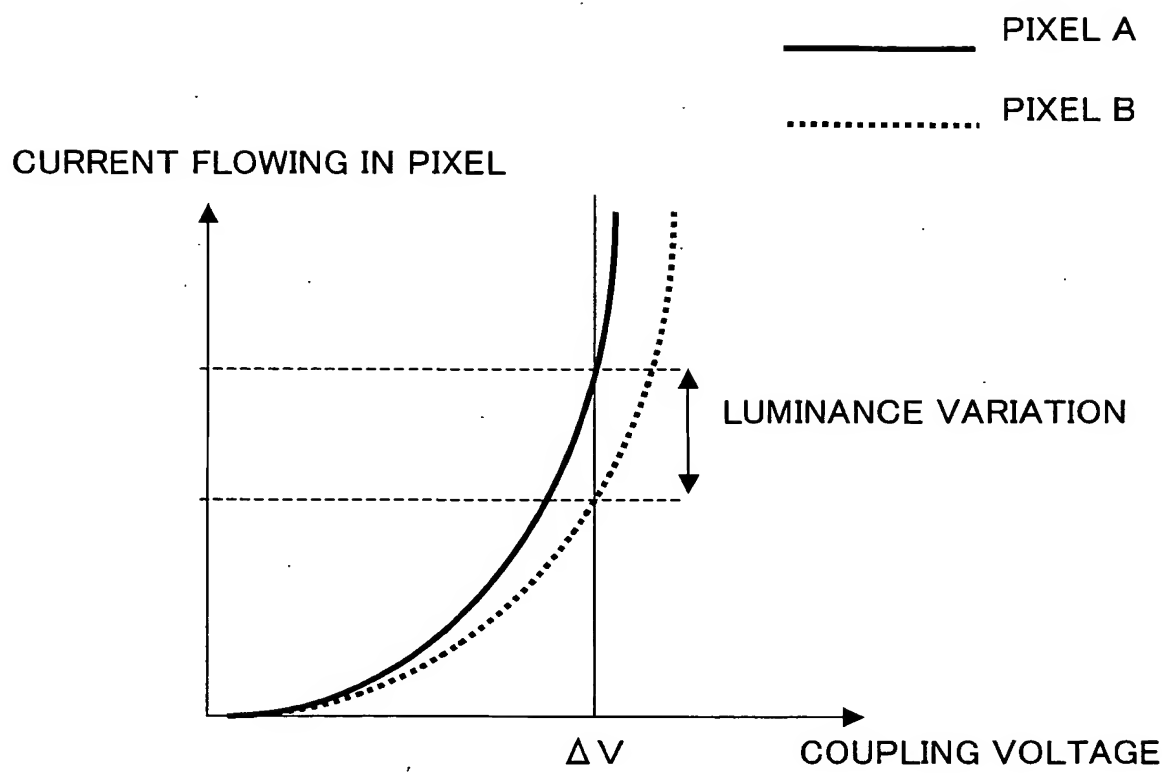


FIG. 17

GATE VOLTAGE OF
DRIVE TRANSISTOR

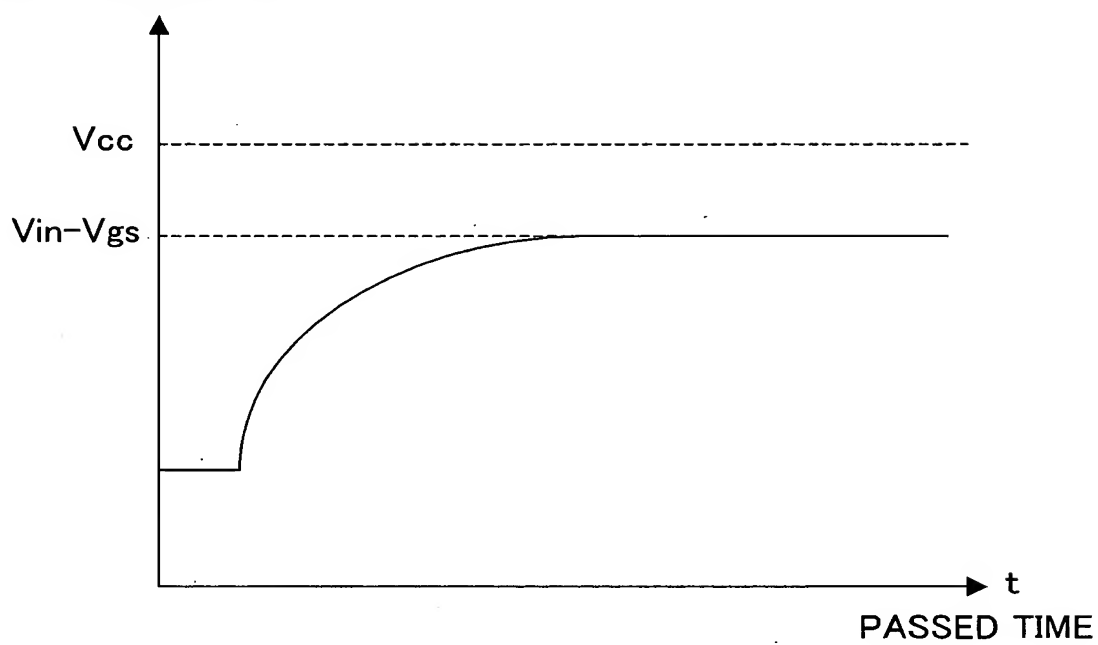


FIG. 18

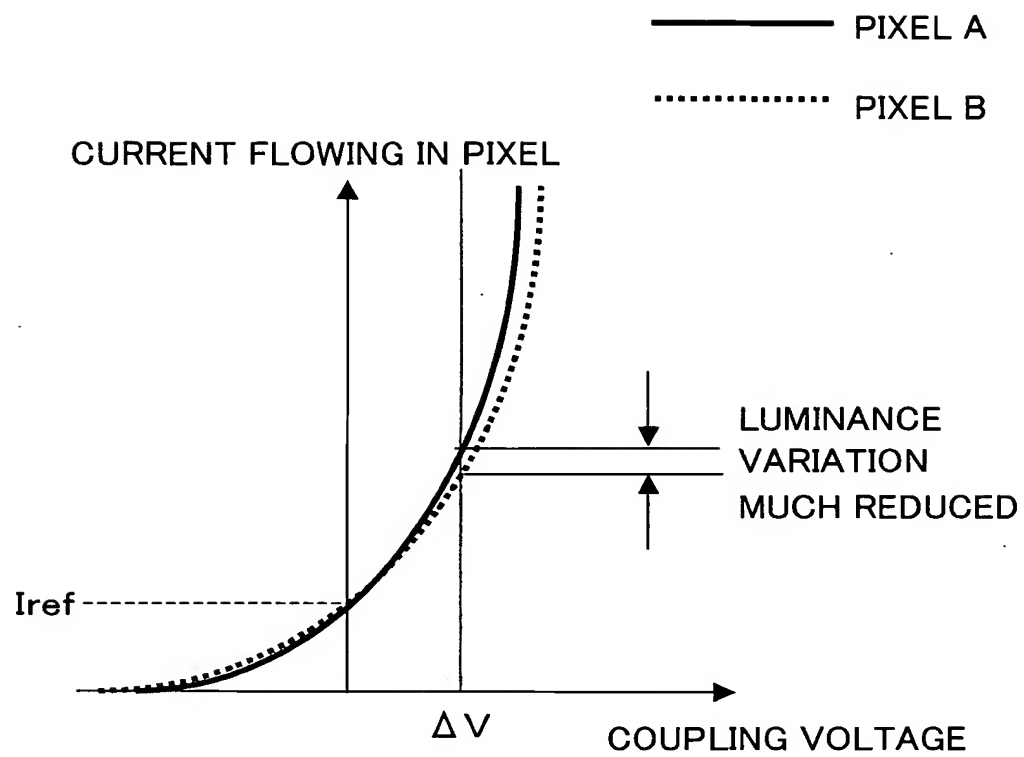


FIG. 19

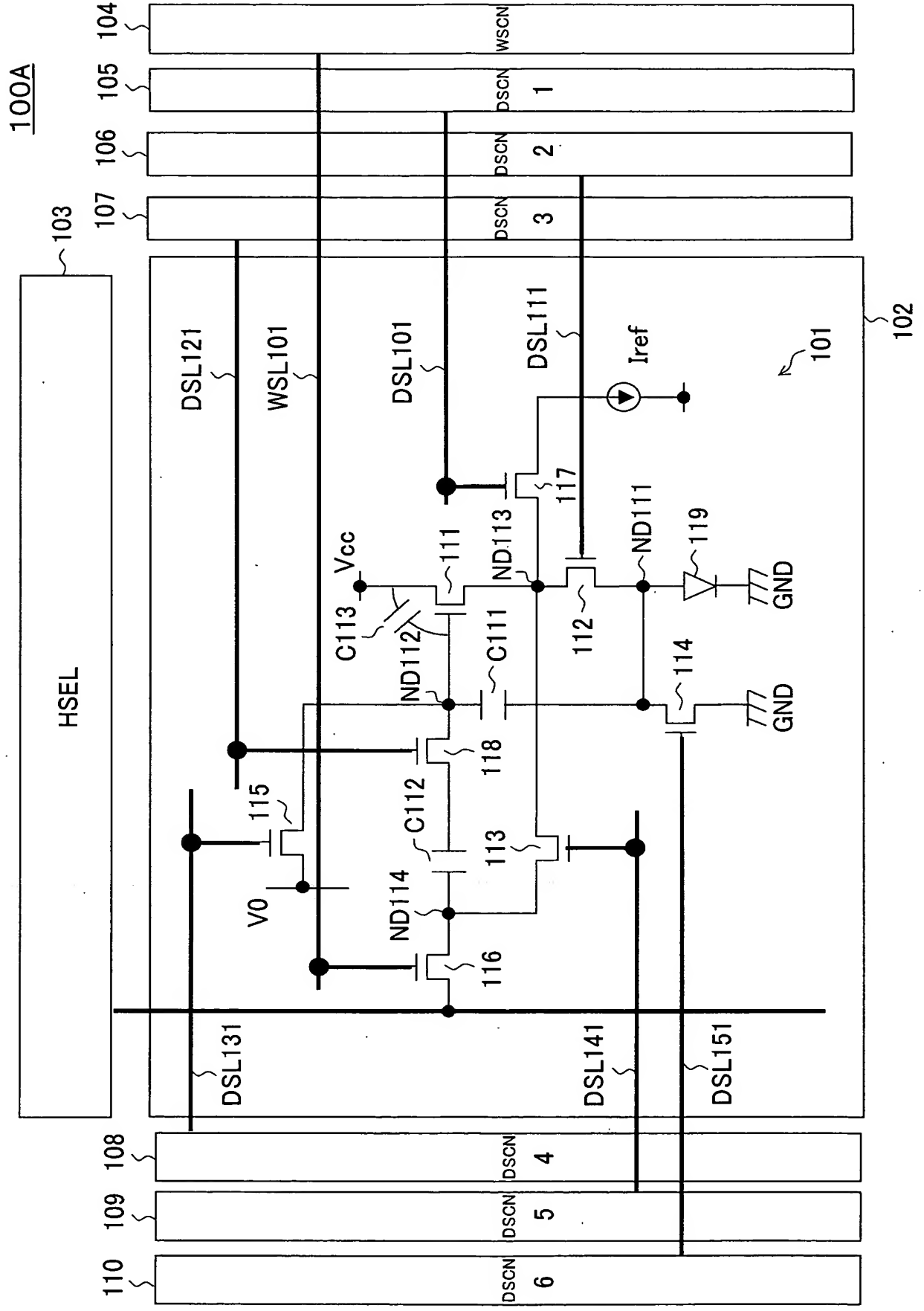


FIG. 20A $ds[4]$ TFT115

FIG. 20B $ws[1]$ TFT116

FIG. 20C $ds[3]$ TFT118

FIG. 20D $ds[5]$ TFT113

FIG. 20E $ds[6]$ TFT114

FIG. 20F $ds[2]$ TFT112

FIG. 20G $ds[1]$ TFT111

FIG. 20H V_{g111}

FIG. 20I V_{ND111}

THRESHOLD VALUE OF ORGANIC EL

GND LEVEL

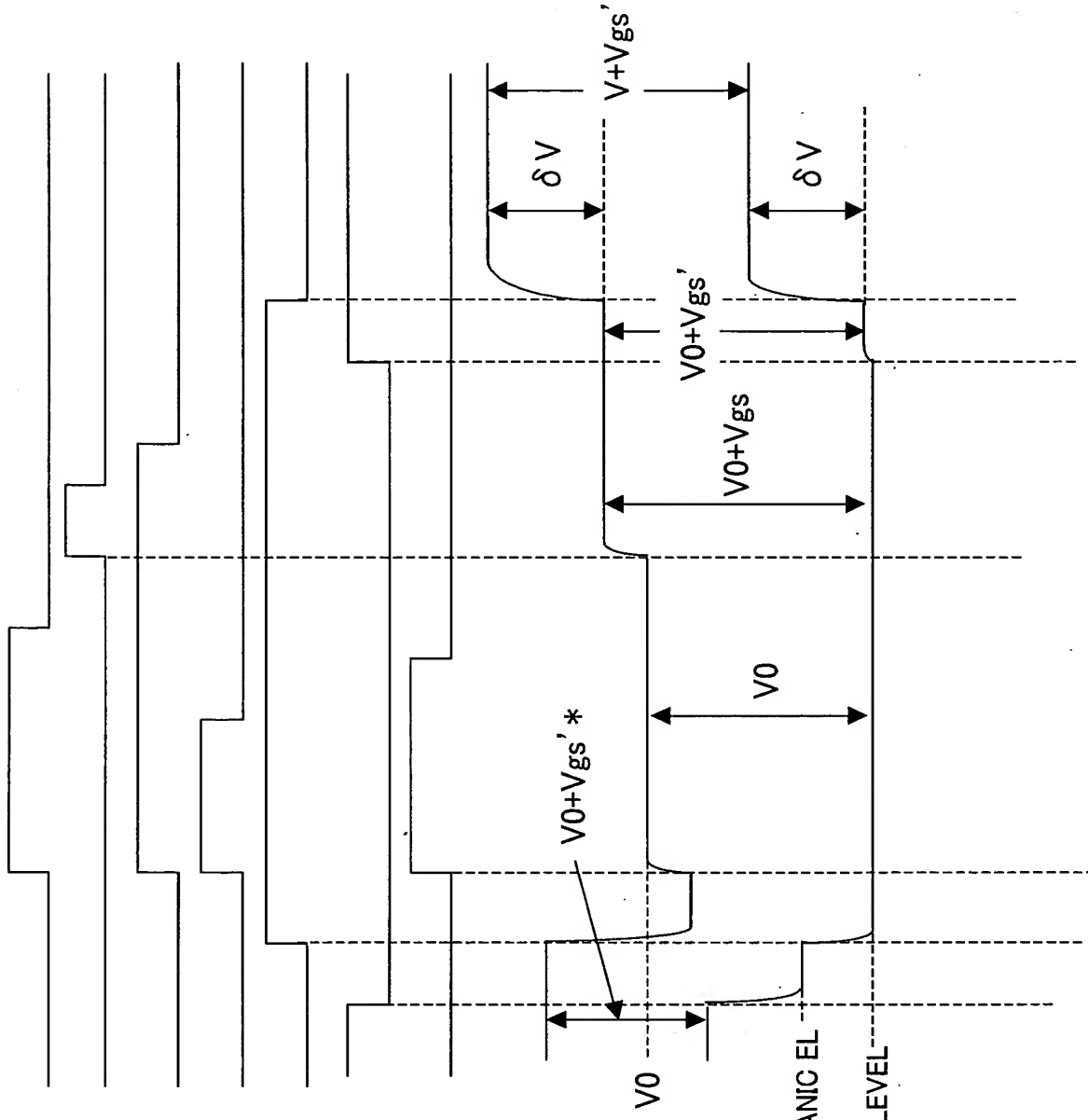
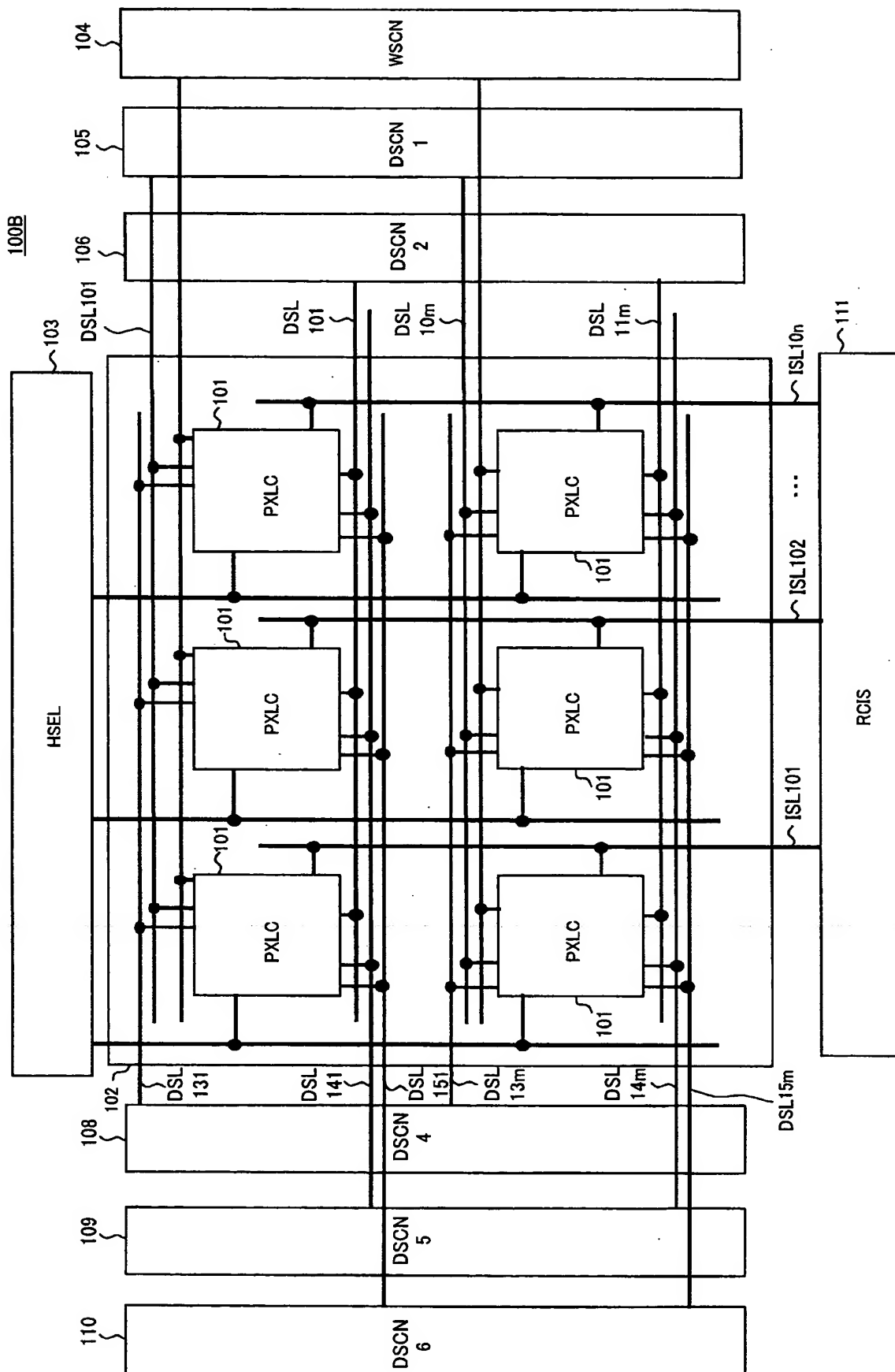


FIG. 21



The diagram illustrates a pixel circuit 100B, which is a crossbar array. It consists of data signal lines (DSCN 1, 2, 4, 5, 6) and word signal lines (WSL 101, DSL 101, DSL 111, DSL 131, DSL 141, DSL 151). The circuit includes transistors 111, 112, 113, 114, 115, 116, 117, 118, 119, capacitors C111, C112, C113, and a current source Iref. The circuit is controlled by HSEL and 103 signals.

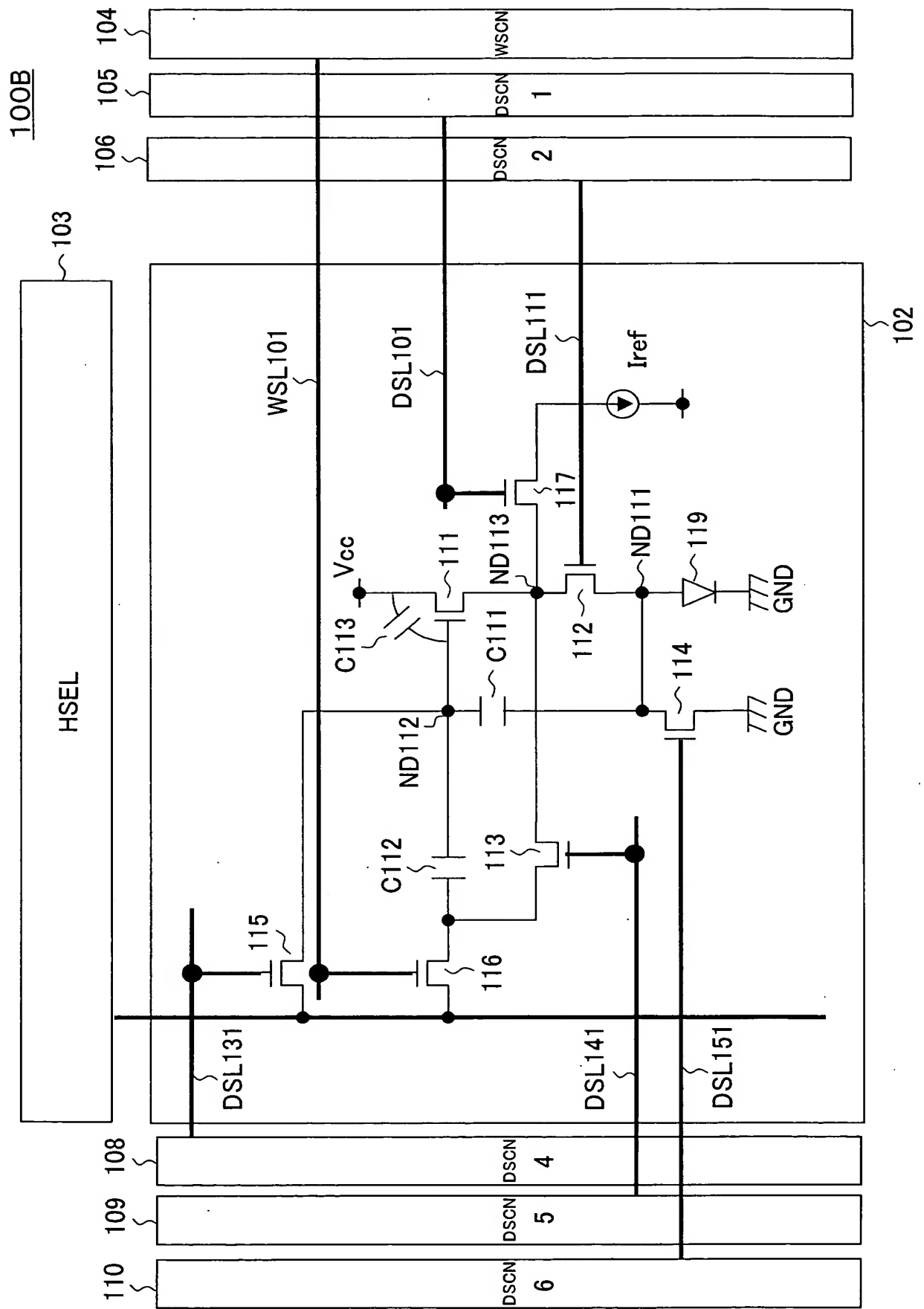


FIG. 23A $ds[4]$ TFT115

FIG. 23B $ws[1]$ TFT116

FIG. 23C $ds[5]$ TFT113

FIG. 23D $ds[6]$ TFT114

FIG. 23E $ds[2]$ TFT112

FIG. 23F $ds[1]$ TFT111

FIG. 23G V_{g111}

FIG. 23H V_{ND111}

THRESHOLD VALUE OF ORGANIC EL

GND LEVEL

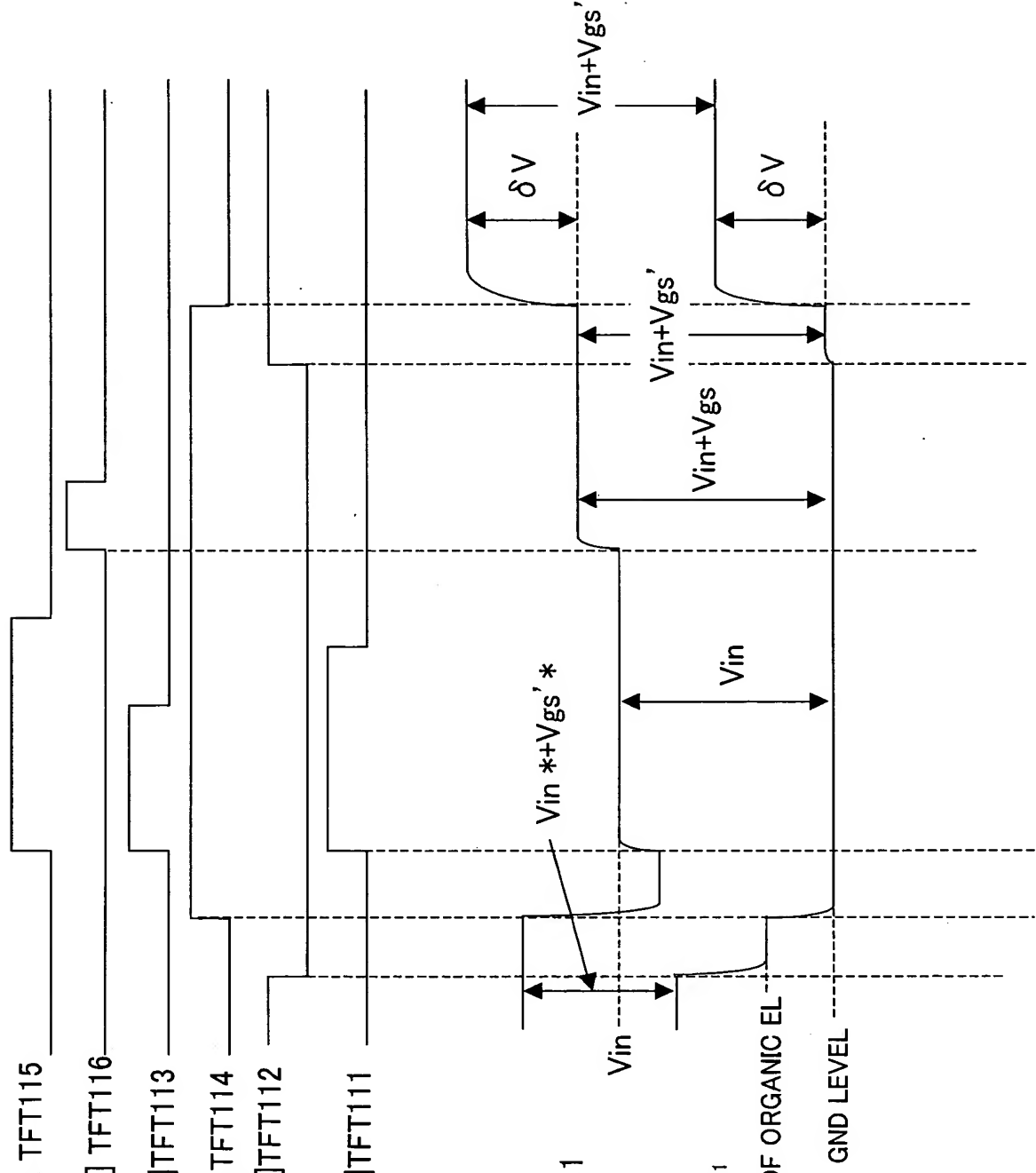


FIG. 24

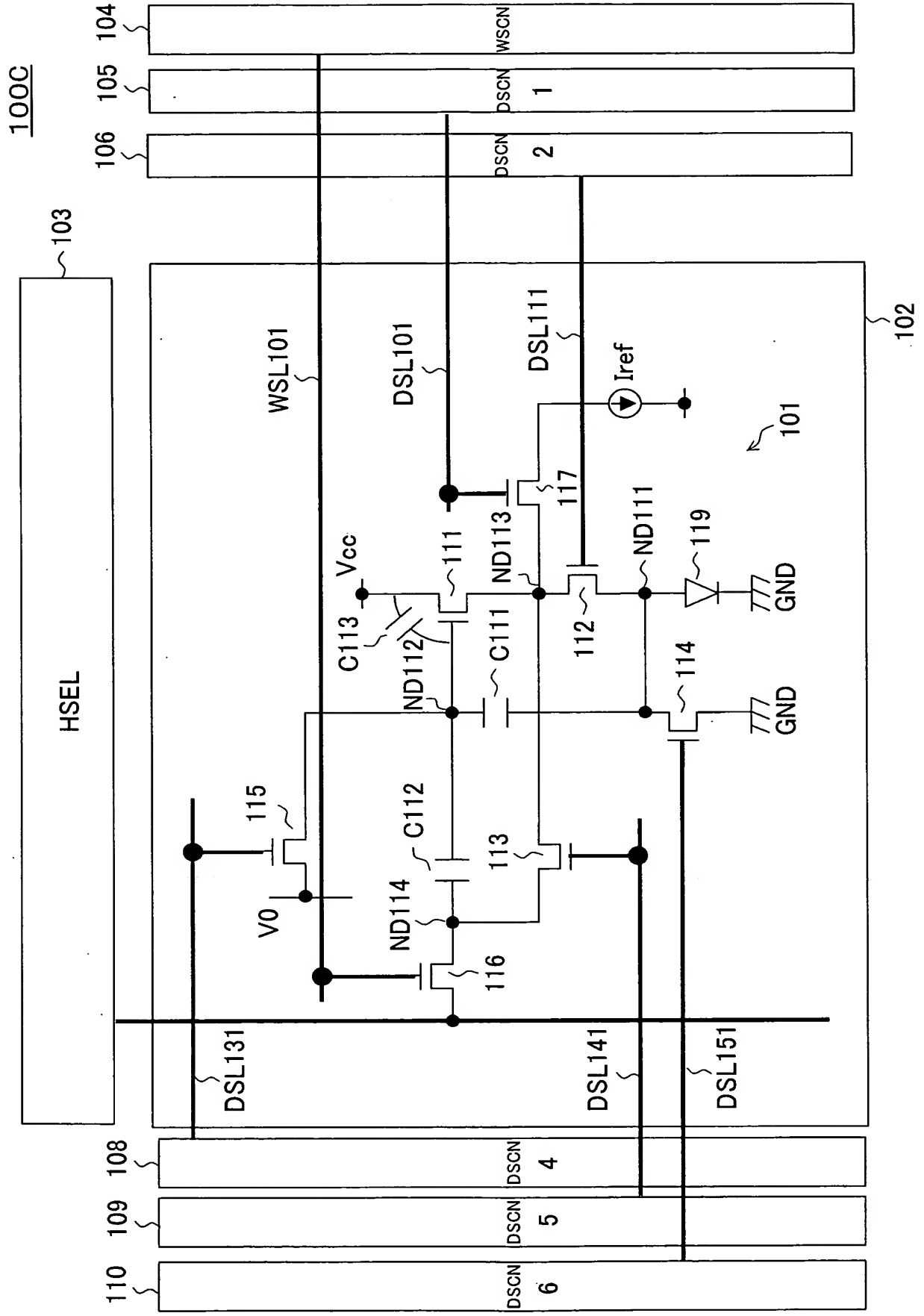


FIG. 25A_{ds[4]} TFT115

FIG. 25B_{ws[1]} TFT116

FIG. 25C_{ds[5]} TFT113

FIG. 25D_{ds[6]} TFT114

FIG. 25E_{ds[2]} TFT112

FIG. 25F_{ds[1]} TFT111

FIG. 25G_{Vg111}

FIG. 25H_{V_{ND111}}
THRESHOLD VALUE OF ORGANIC EL

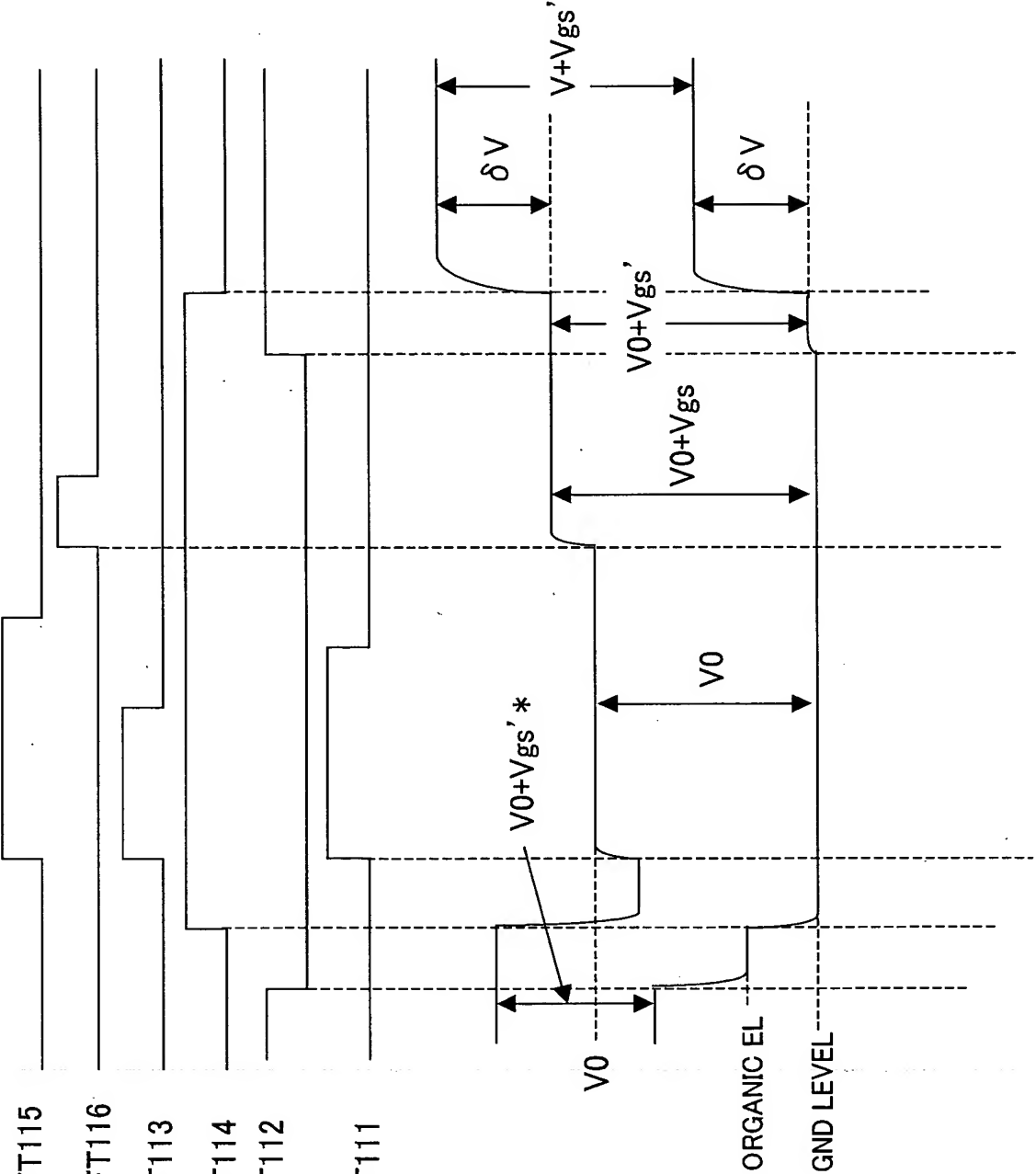
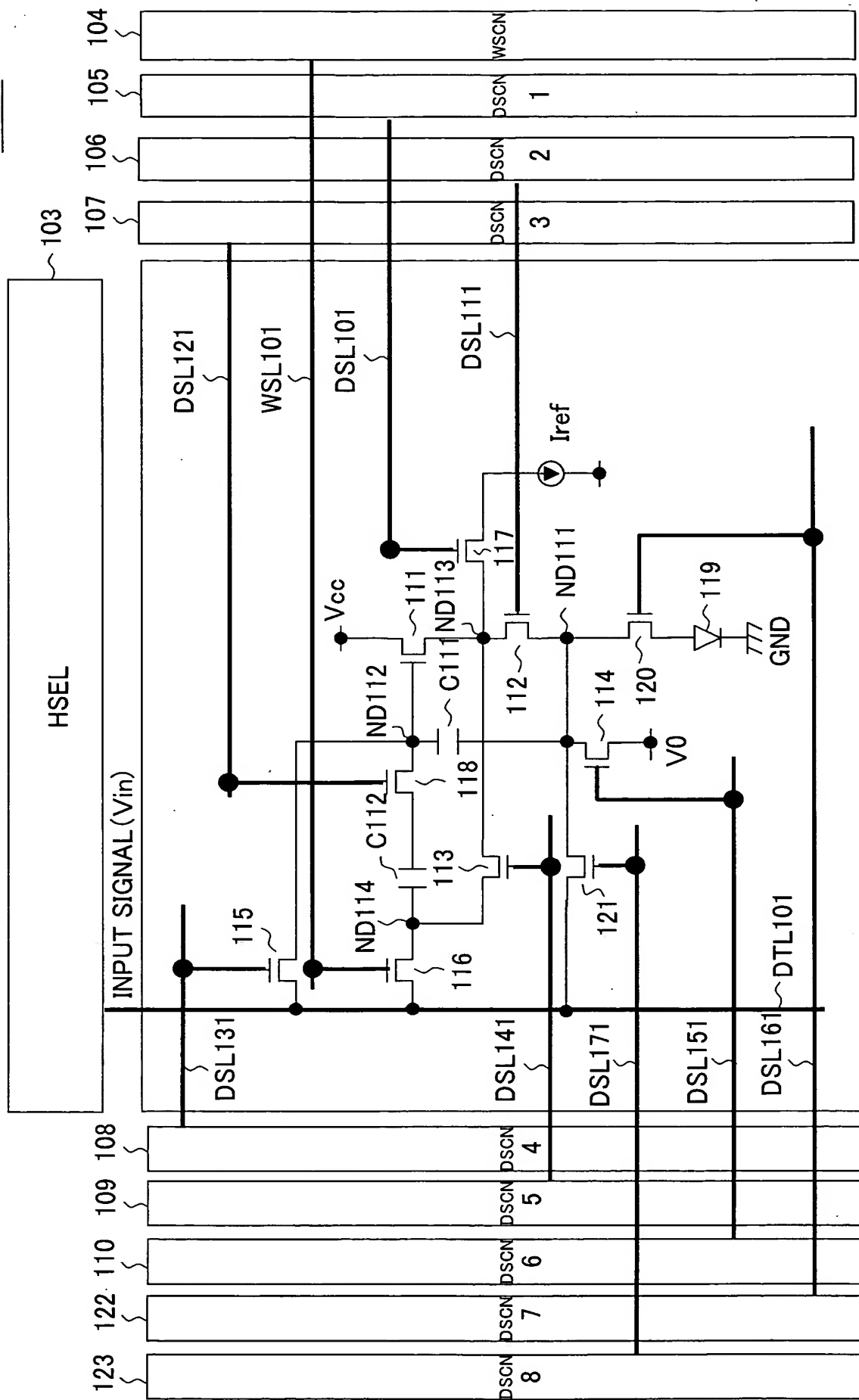


FIG. 26

100D



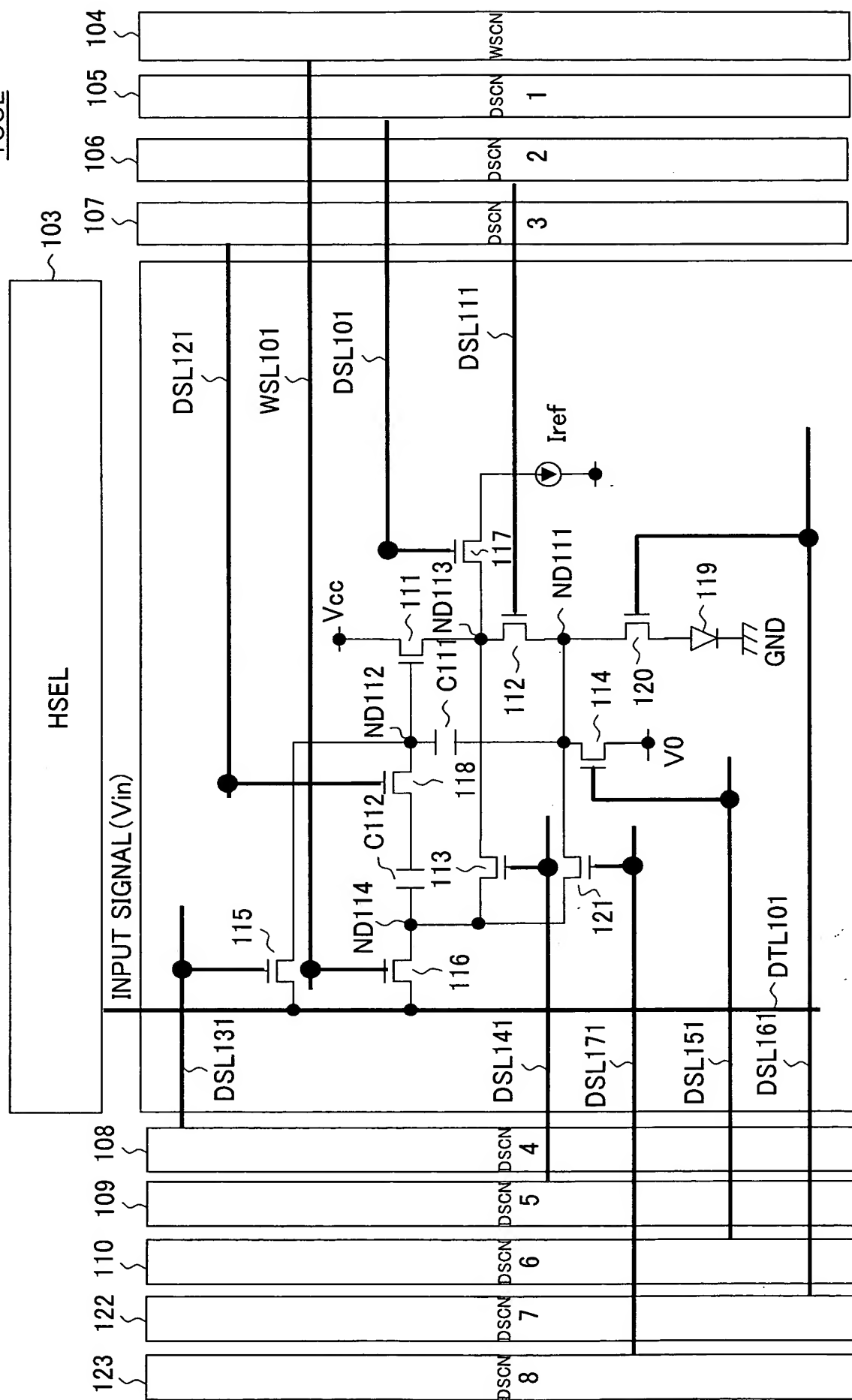
100E

FIG. 28A_{ds[4]} TFT115

FIG. 28B_{ws[1]} TFT116

FIG. 28C_{ds[3]} TFT118

FIG. 28D_{ds[5]} TFT113

FIG. 28E_{ds[2]} TFT112

FIG. 28F_{ds[1]} TFT117

FIG. 28G_{ds[7]} TFT120

FIG. 28H_{ds[6]} TFT114

FIG. 28I_{ds[8]} TFT121

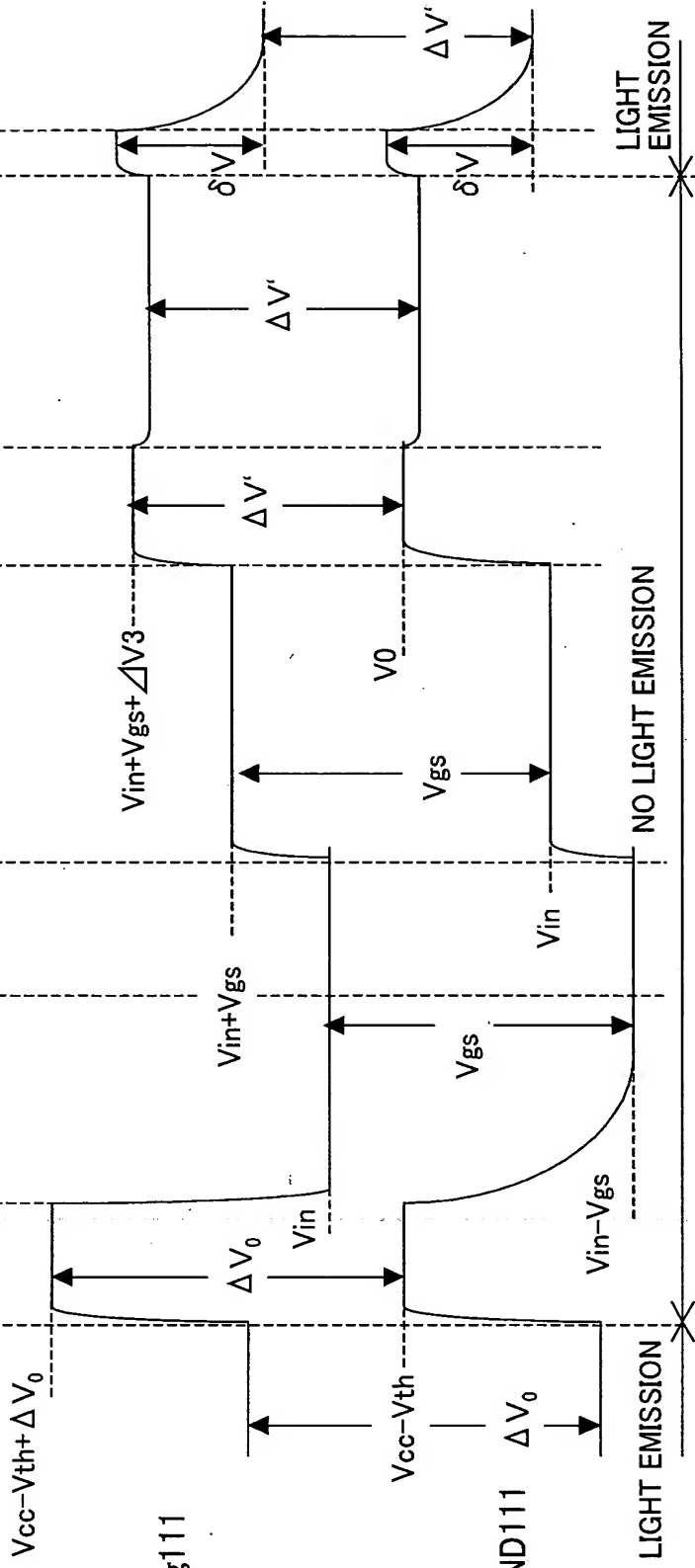


FIG. 28K_{VND111}

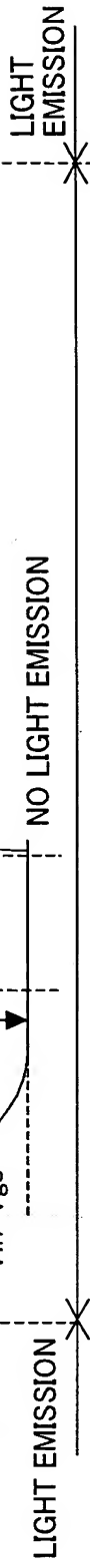


FIG. 29A ds[4] TFT115

FIG. 29B ws[1] TFT116

FIG. 29C ds[3]TFT118

FIG. 29D ds[5]TFT113

FIG. 29E ds[2]TFT112

FIG. 29F ds[1]TFT117

FIG. 29G ds[7]TFT120

FIG. 29H ds[6]TFT114

FIG. 29I ds[8]TFT121

FIG. 29J Vg111

FIG. 29K VND11

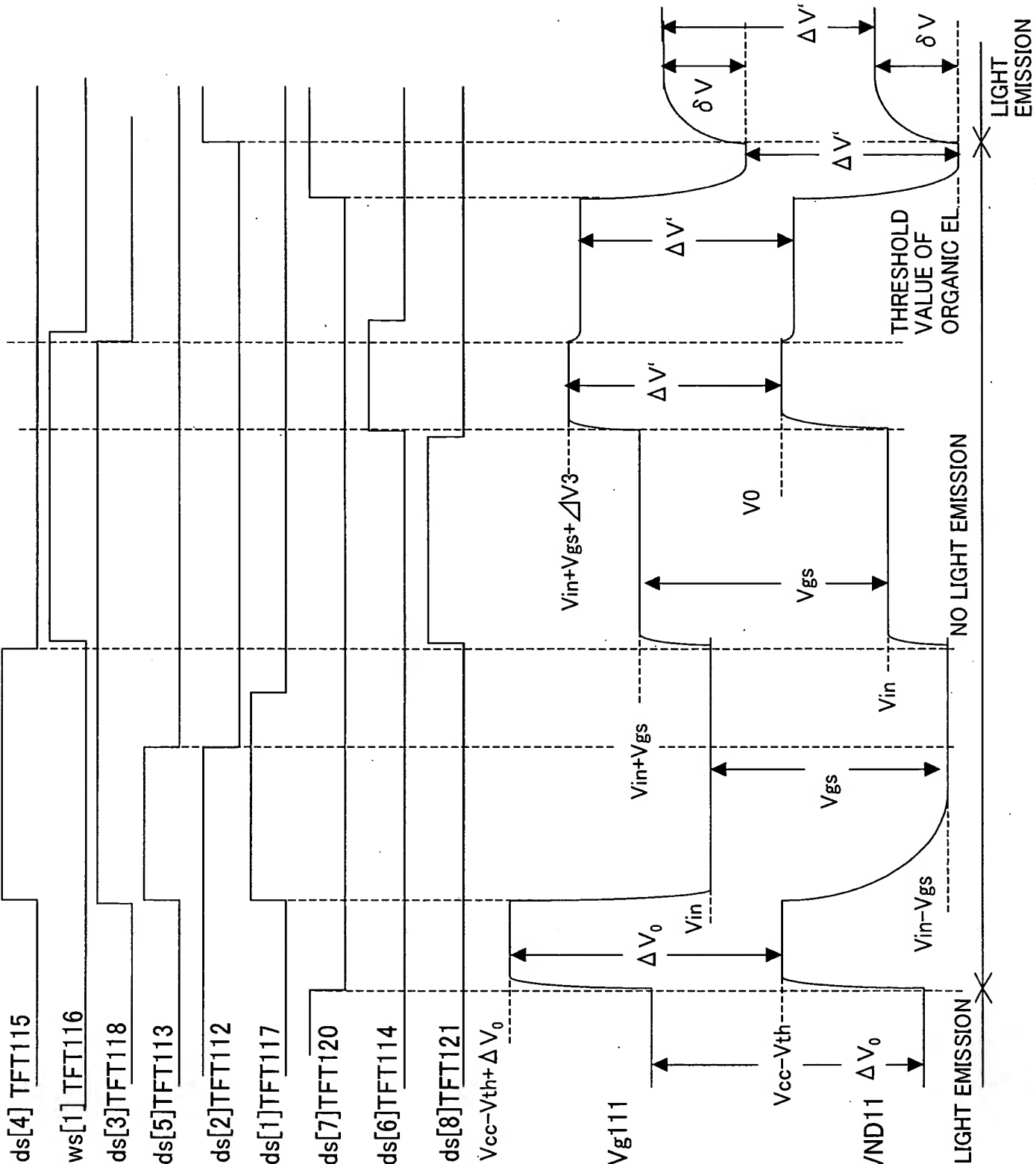


FIG. 30A

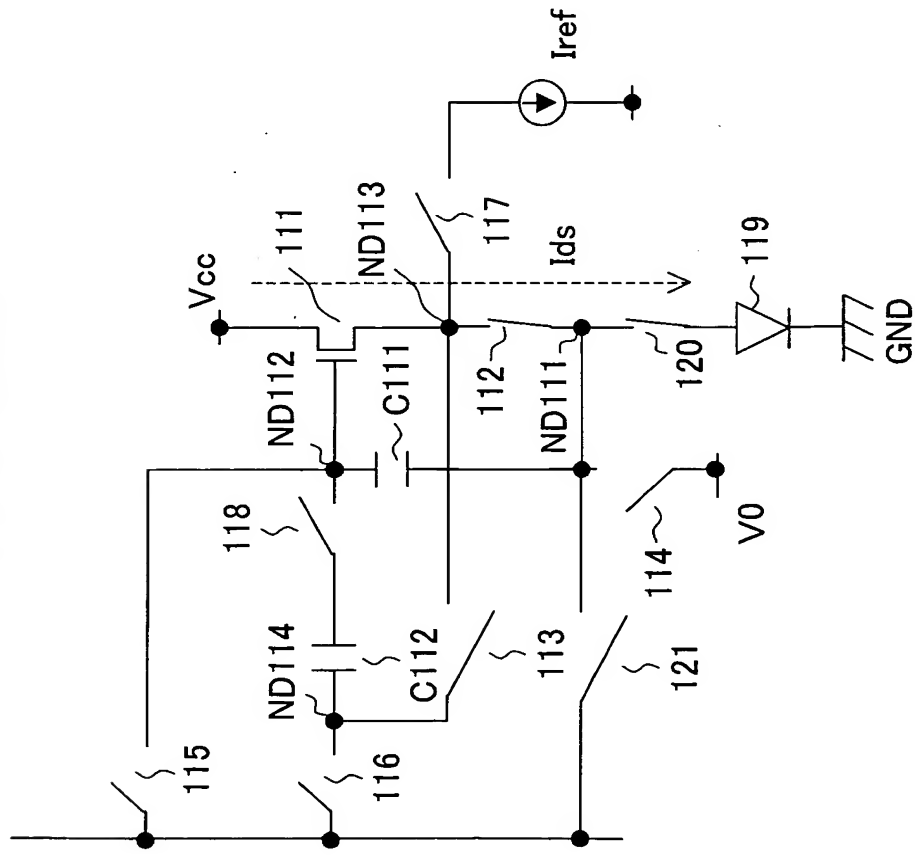


FIG. 30B

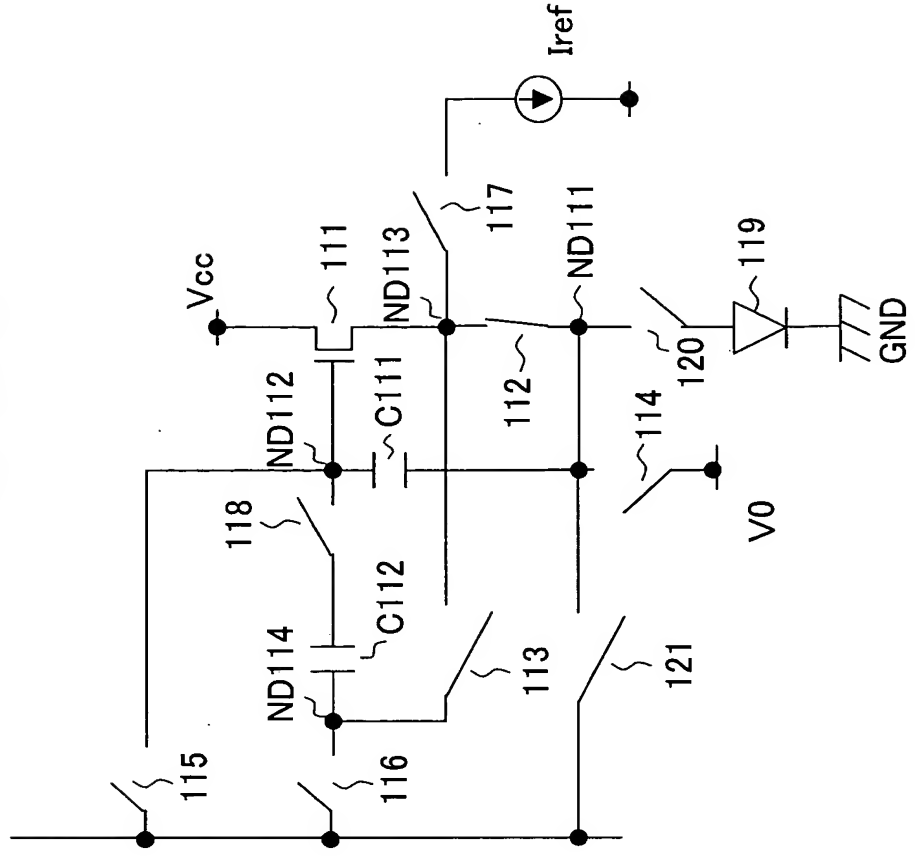


FIG. 33A

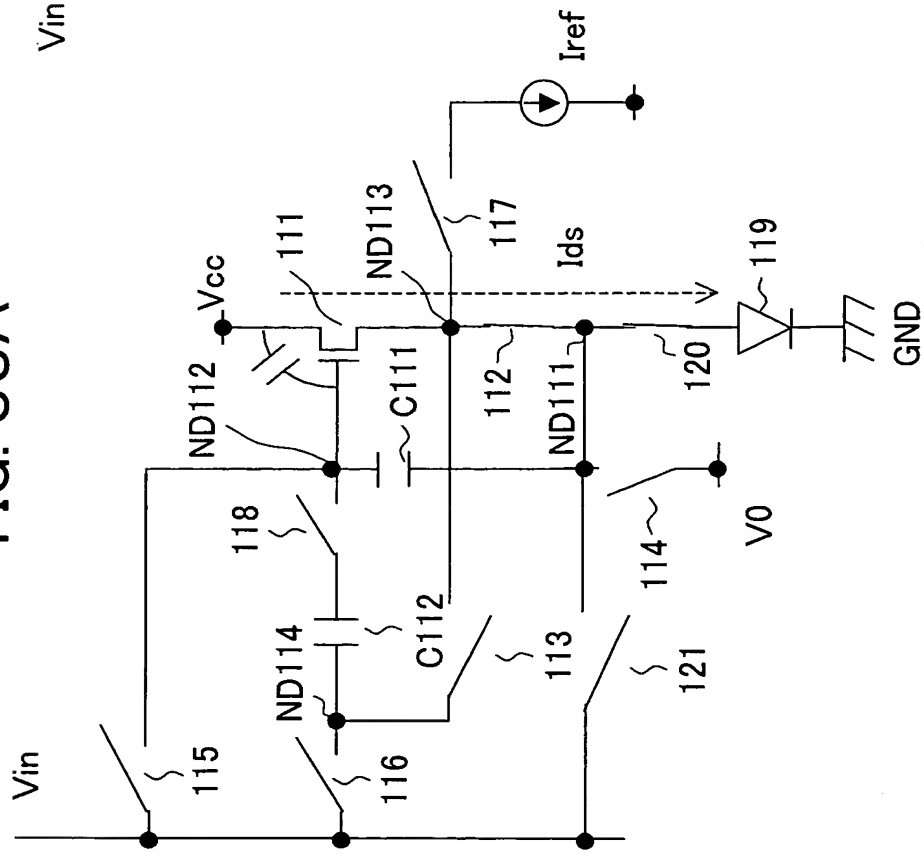


FIG. 33B

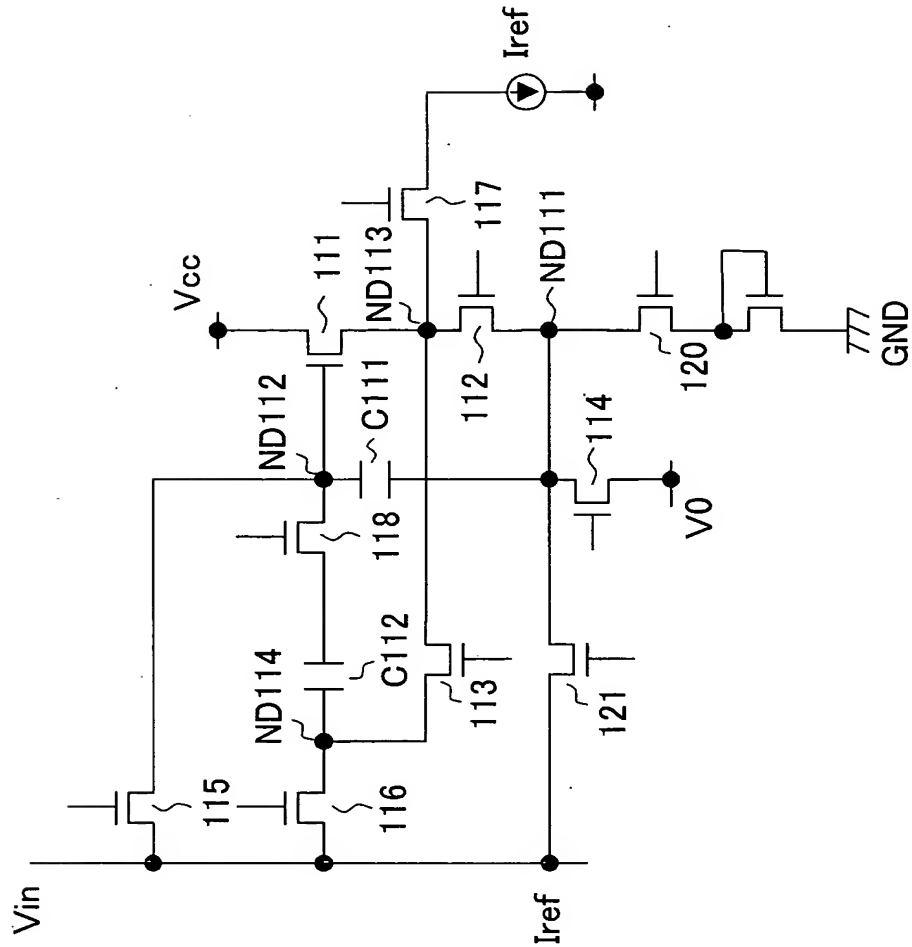


FIG. 34

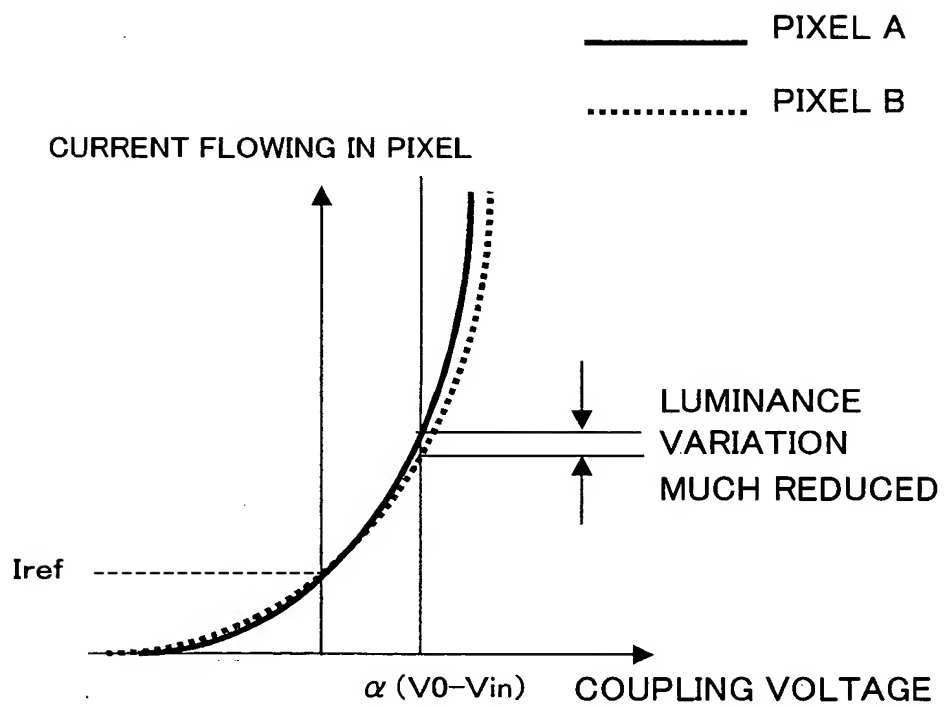


FIG. 35

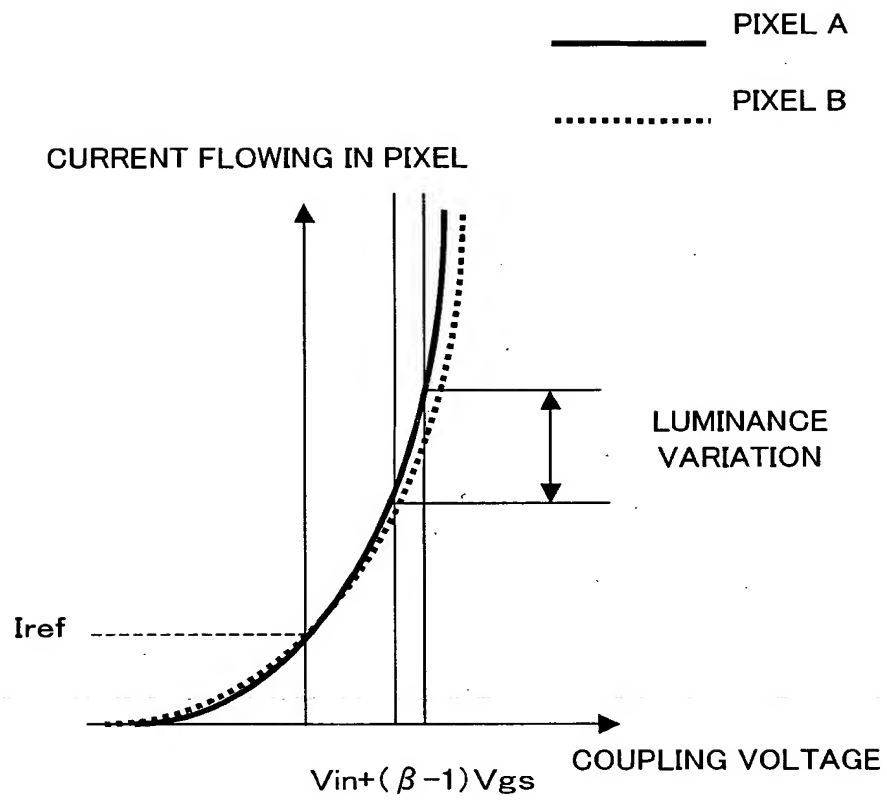


FIG. 36

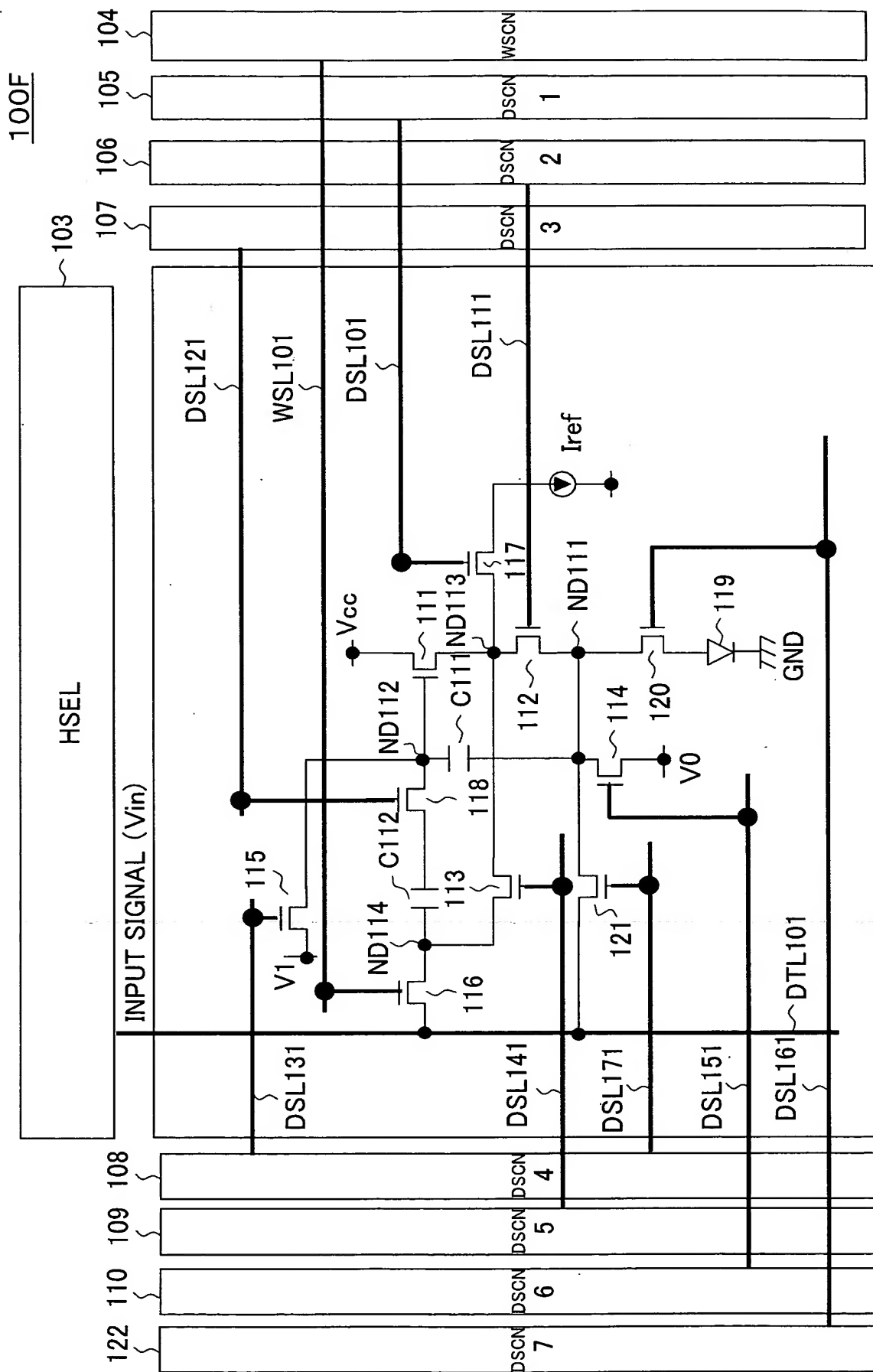


FIG. 37

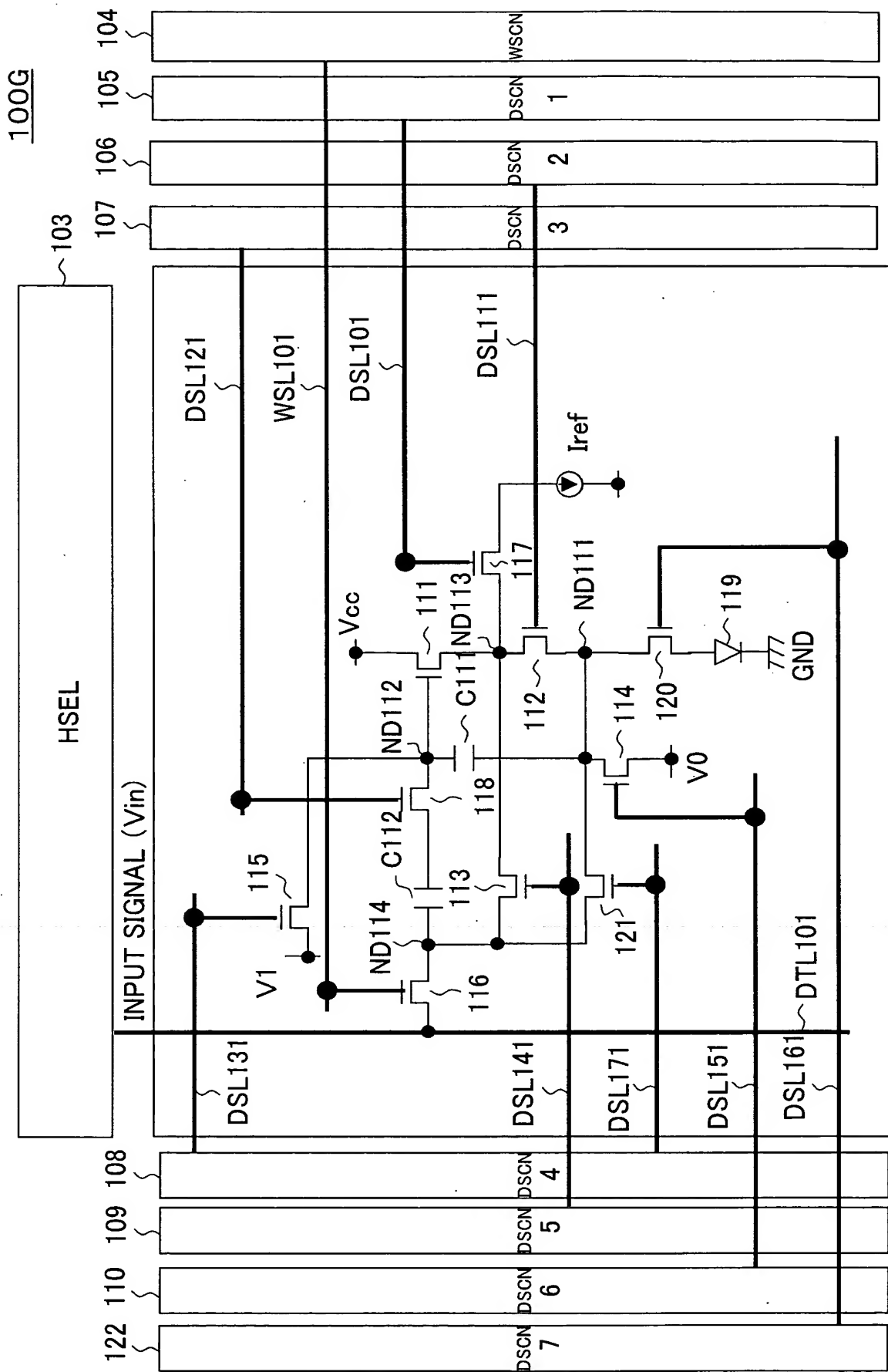


FIG. 38A_{ds[4]} TFT115

FIG. 38B_{ws[1]} TFT116

FIG. 38C_{ds[3]} TFT118

FIG. 38D_{ds[5]} TFT113

FIG. 38E_{ds[2]} TFT112

FIG. 38F_{ds[1]} TFT117

FIG. 38G_{ds[7]} TFT120

FIG. 38H_{ds[6]} TFT114

FIG. 38I_{ds[8]} TFT121

FIG. 38J_{Vg111}

FIG. 38K_{VND111}

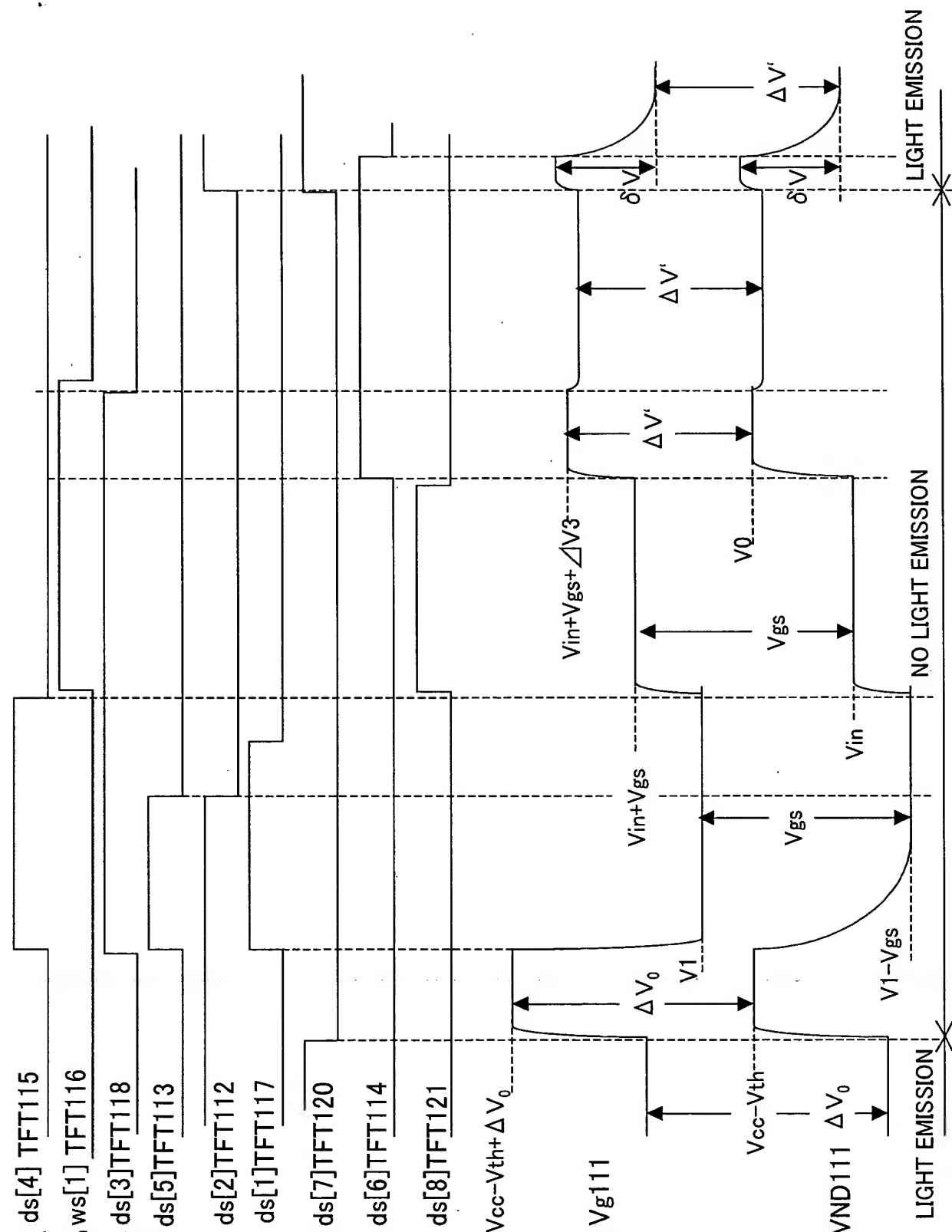


FIG. 39A $ds[4]$ TFT115

FIG. 39B $ws[1]$ TFT116

FIG. 39C $ds[3]$ TFT118

FIG. 39D $ds[5]$ TFT113

FIG. 39E $ds[2]$ TFT112

FIG. 39F $ds[1]$ TFT117

FIG. 39G $ds[7]$ TFT120

FIG. 39H $ds[6]$ TFT114

FIG. 39I $ds[8]$ TFT121

FIG. 39J V_{g111}

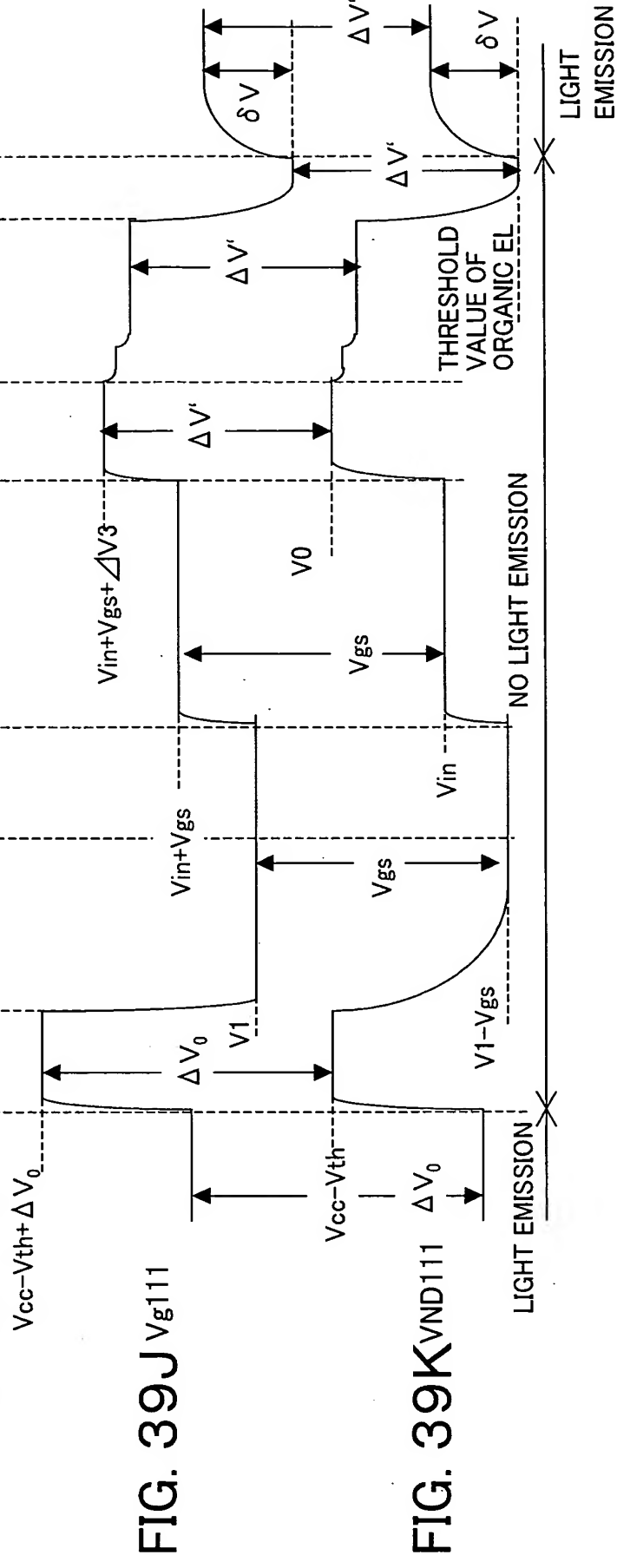
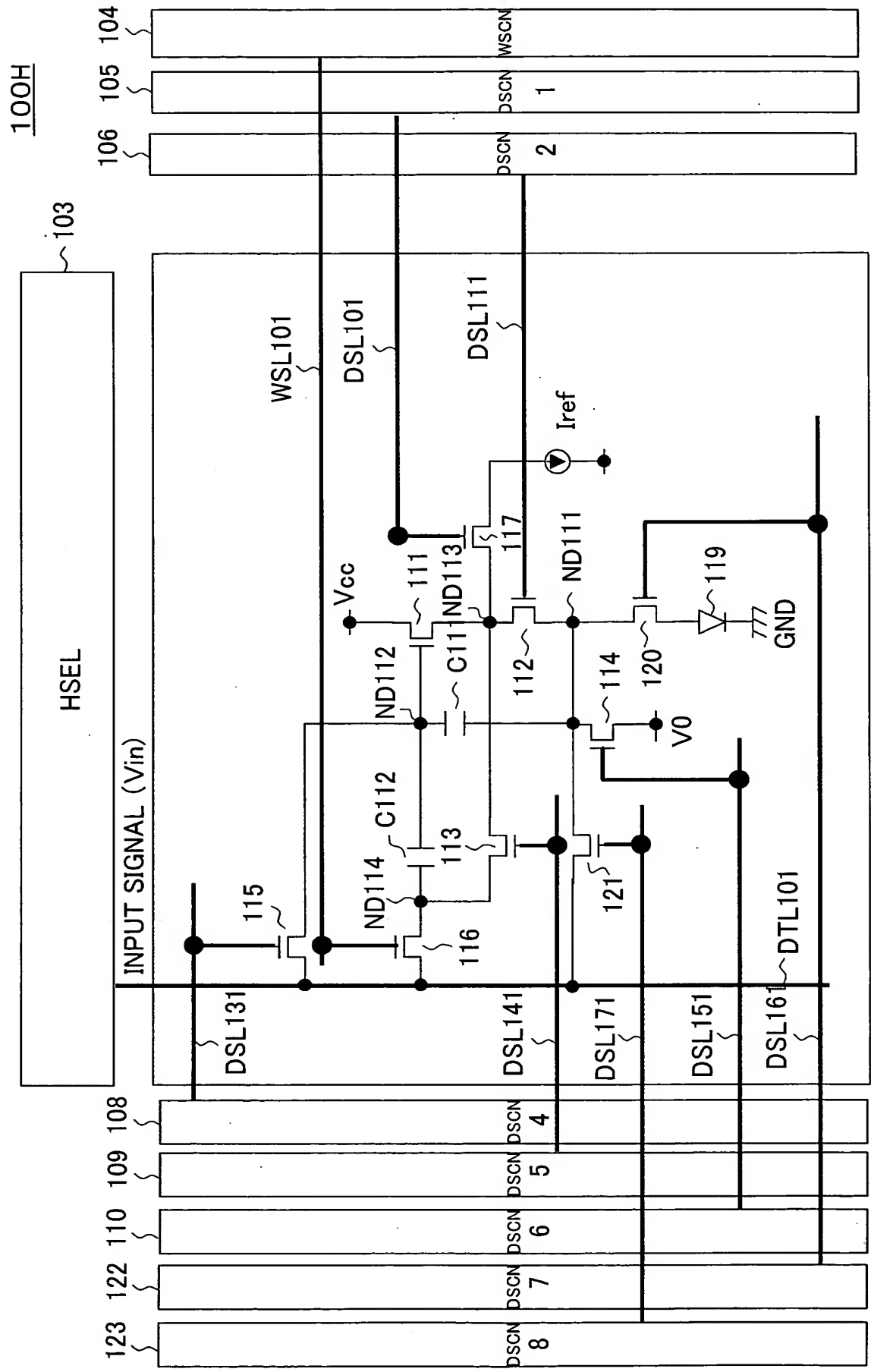


FIG. 39K V_{ND111}

FIG. 40



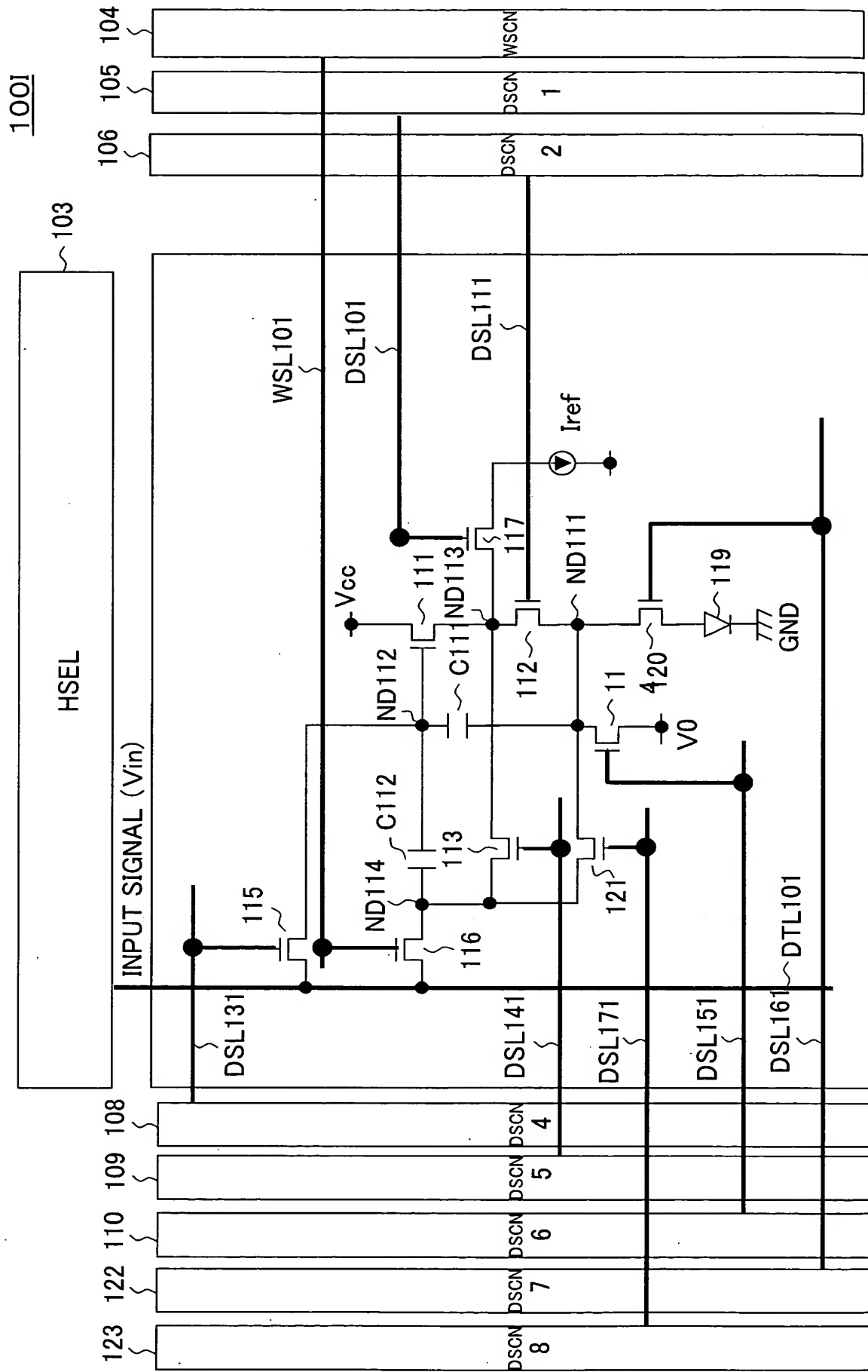


FIG. 42A_{ds[4]} TFT115

FIG. 42B_{ws[1]} TFT116

FIG. 42C_{ds[5]} TFT113

FIG. 42D_{ds[2]} TFT112

FIG. 42E_{ds[1]} TFT117

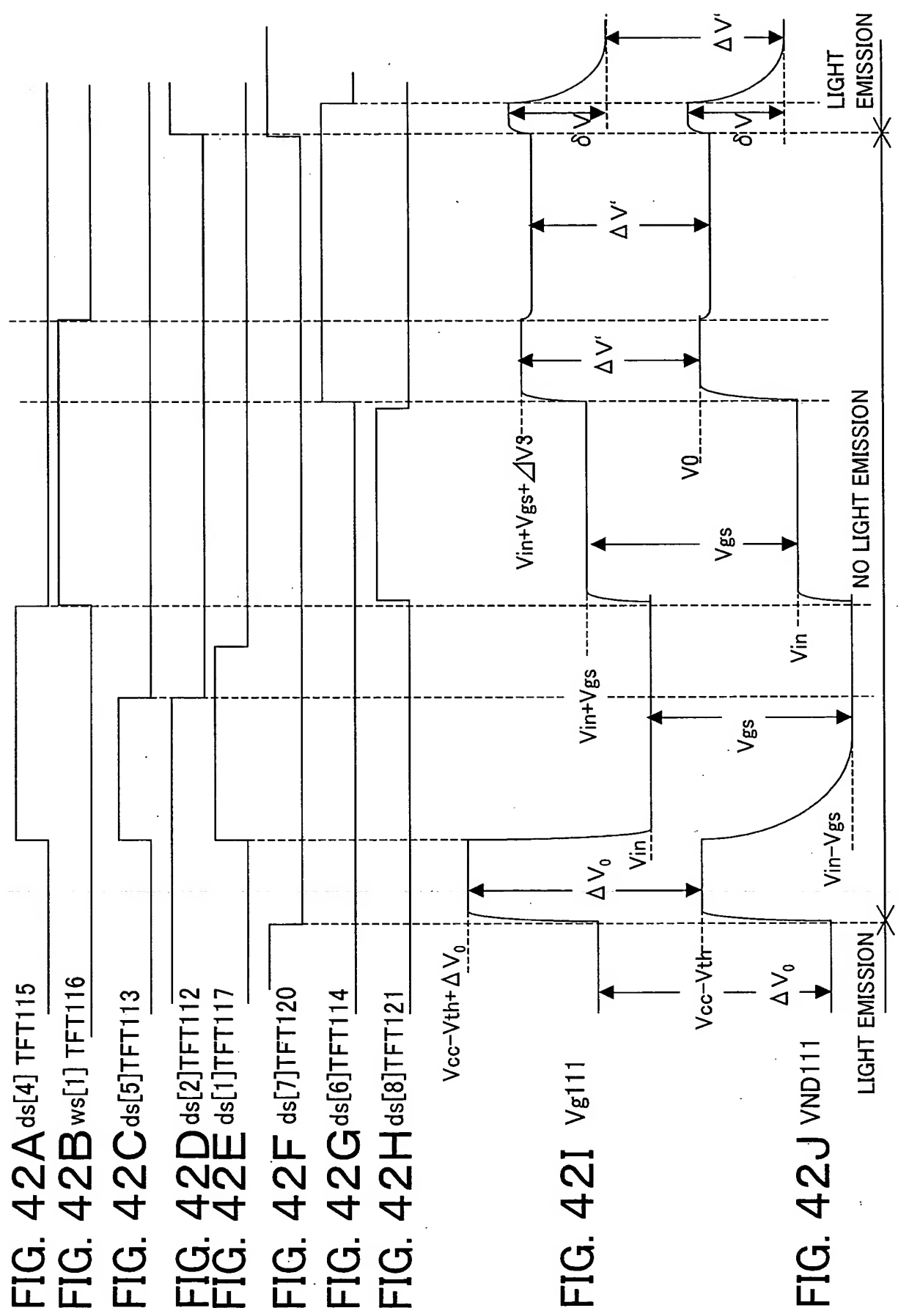
FIG. 42F_{ds[7]} TFT120

FIG. 42G_{ds[6]} TFT114

FIG. 42H_{ds[8]} TFT121

FIG. 42I_{Vg111}

FIG. 42J_{VND111}



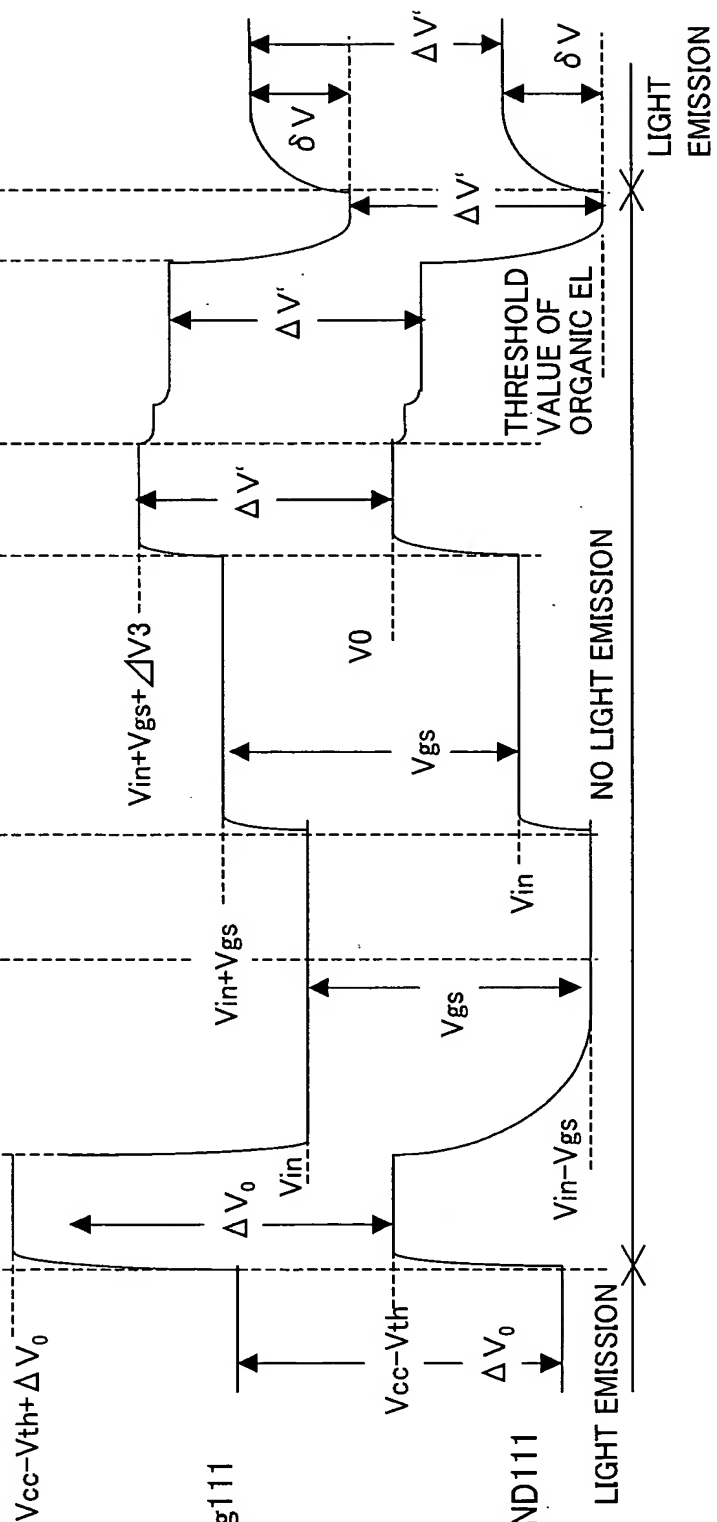
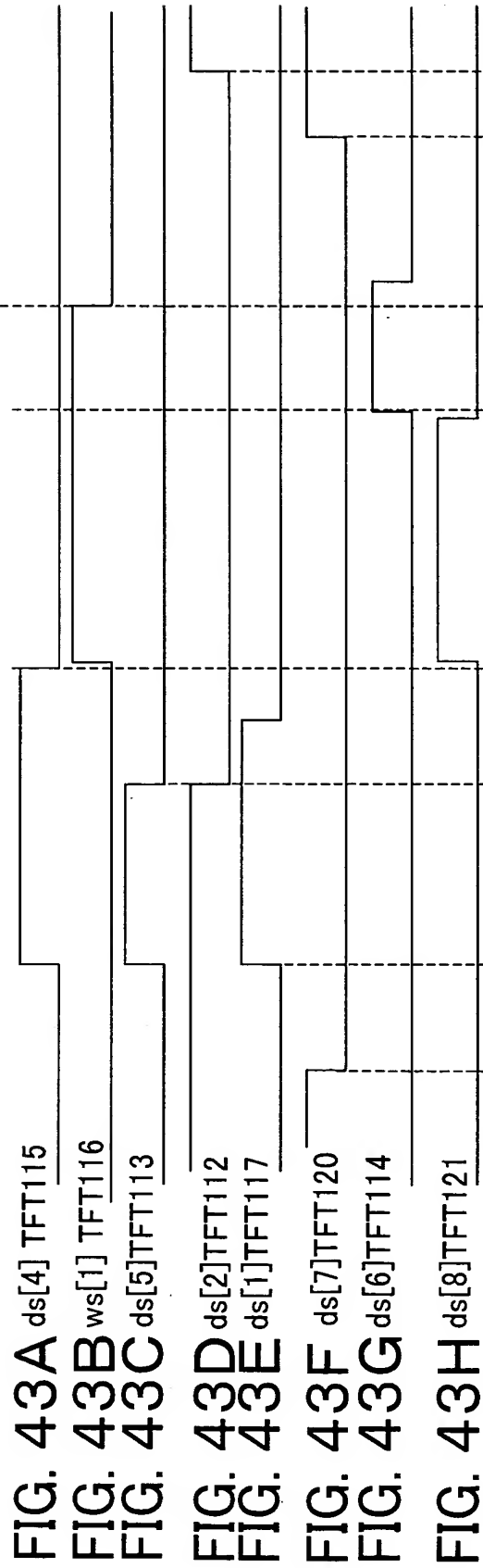


FIG. 44

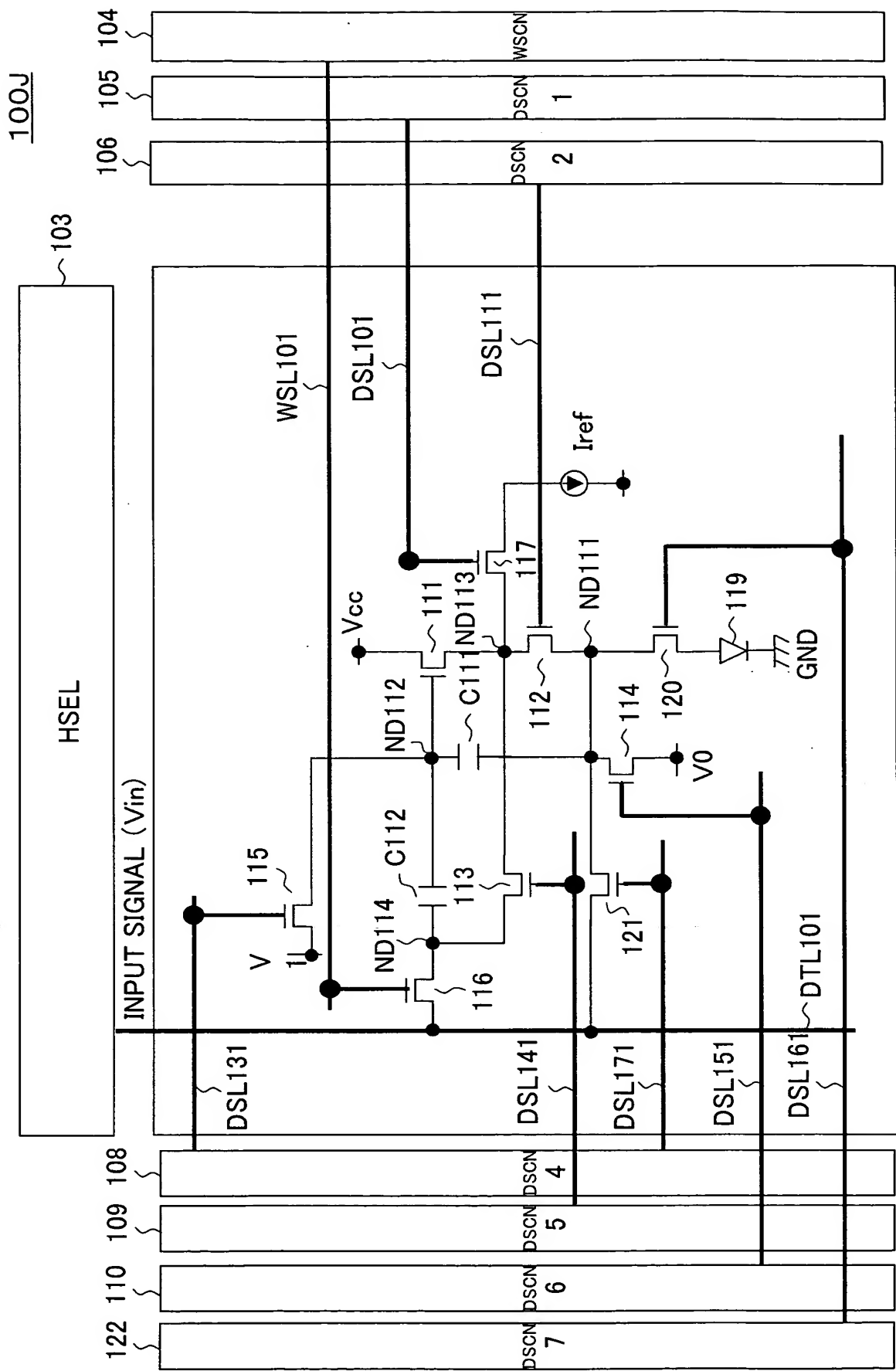


FIG. 45

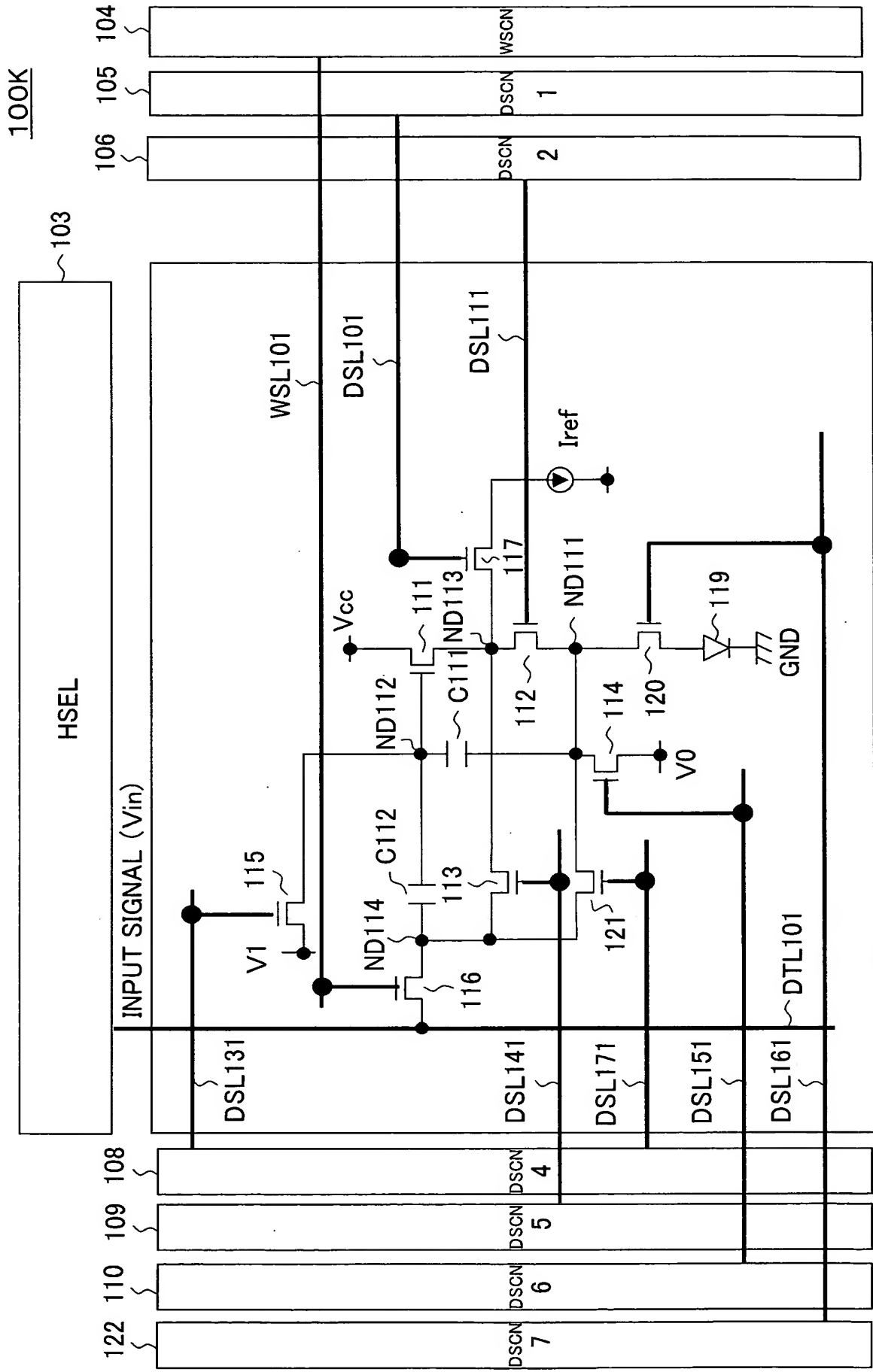


FIG. 46A_{ds[4]} TFT115

FIG. 46B_{ws[1]} TFT116

FIG. 46C_{ds[5]} TFT113

FIG. 46D_{ds[2]} TFT112

FIG. 46E_{ds[1]} TFT117

FIG. 46F_{ds[7]} TFT120

FIG. 46G_{ds[6]} TFT114

FIG. 46H_{ds[8]} TFT121

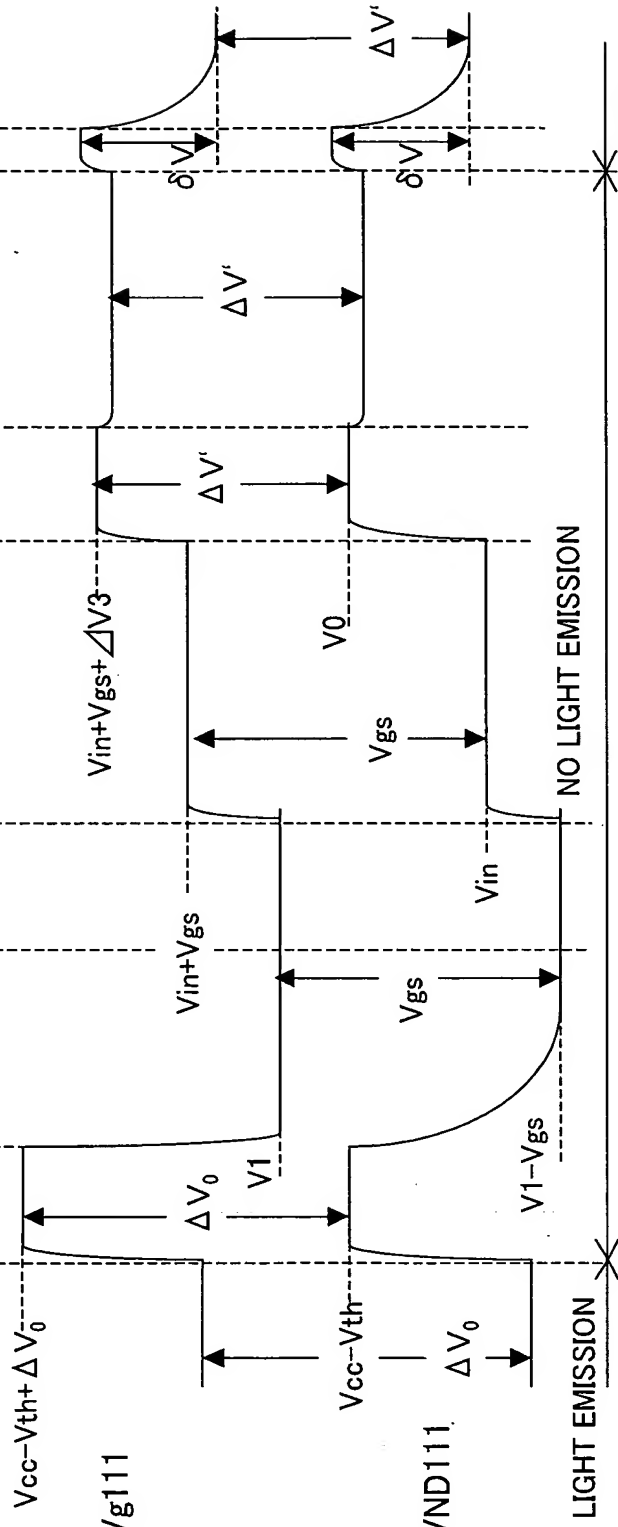


FIG. 46I V_{g111}

FIG. 46J V_{ND111}

